

VERILOG PROJECT

VEDIC MULTIPLIER ✕



8-Bit Multiplier

```
module vedic_8bits_8x8 {
    input [7:0]a;
    input [7:0]b;
    output [15:0]c;

    wire [15:0]p0;
    wire [15:0]p1;
    wire [15:0]p2;
    wire [15:0]p3;
    wire [15:0]p4;
    wire [15:0]p5;
    wire [15:0]p6;
    wire [15:0]p7;
    wire [15:0]p8;
    wire [15:0]p9;
    wire [15:0]p10;
    wire [15:0]p11;
    wire [15:0]p12;
    wire [15:0]p13;
    wire [15:0]p14;
    wire [15:0]p15;

    assign p0=a[0]&b[0];
    assign p1=a[0]&b[1];
    assign p2=a[0]&b[2];
    assign p3=a[0]&b[3];
    assign p4=a[0]&b[4];
    assign p5=a[0]&b[5];
    assign p6=a[0]&b[6];
    assign p7=a[0]&b[7];
    assign p8=a[1]&b[0];
    assign p9=a[1]&b[1];
    assign p10=a[1]&b[2];
    assign p11=a[1]&b[3];
    assign p12=a[1]&b[4];
    assign p13=a[1]&b[5];
    assign p14=a[1]&b[6];
    assign p15=a[1]&b[7];

    assign temp0=(p0<16'b00000000)?0:1;
    add_8_bits_0to16(temp0,p1);
    assign temp1=(p1<16'b00000000)?temp0:0;
    assign temp2=(p2<16'b00000000)?0:1;
    assign temp3=(p3<16'b00000000)?temp2:0;
    add_16_bits_0to16(temp0,temp1);
    assign temp4=(p4<16'b00000000)?0:1;
    add_16_bits_0to16(temp2,temp3);

    add_16_bits_0to16(temp4,temp1);

    assign c[0]=p0;
    assign c[1]=p1;
    assign c[2]=p2;
    assign c[3]=p3;
    assign c[4]=p4;
    assign c[5]=p5;
    assign c[6]=p6;
    assign c[7]=p7;
    assign c[8]=p8;
    assign c[9]=p9;
    assign c[10]=p10;
    assign c[11]=p11;
    assign c[12]=p12;
    assign c[13]=p13;
    assign c[14]=p14;
    assign c[15]=p15;
}
```

(Code for 8 bit multiplier)

4-Bit Multiplier

```
module vedic_4x4_4bits_4x4 {
    input [3:0]a;
    input [3:0]b;
    output [7:0]c;

    wire [7:0]p0;
    wire [7:0]p1;
    wire [7:0]p2;
    wire [7:0]p3;
    wire [7:0]p4;
    wire [7:0]p5;
    wire [7:0]p6;
    wire [7:0]p7;
    wire [7:0]p8;
    wire [7:0]p9;
    wire [7:0]p10;
    wire [7:0]p11;
    wire [7:0]p12;
    wire [7:0]p13;
    wire [7:0]p14;
    wire [7:0]p15;

    assign p0=a[0]&b[0];
    assign p1=a[0]&b[1];
    assign p2=a[0]&b[2];
    assign p3=a[0]&b[3];
    assign p4=a[1]&b[0];
    assign p5=a[1]&b[1];
    assign p6=a[1]&b[2];
    assign p7=a[1]&b[3];
    assign p8=a[2]&b[0];
    assign p9=a[2]&b[1];
    assign p10=a[2]&b[2];
    assign p11=a[2]&b[3];
    assign p12=a[3]&b[0];
    assign p13=a[3]&b[1];
    assign p14=a[3]&b[2];
    assign p15=a[3]&b[3];

    assign temp0=(p0<4'b0000)?0:1;
    add_4_bits_0to4(temp0,p1);
    assign temp1=(p1<4'b0000)?temp0:0;
    assign temp2=(p2<4'b0000)?0:1;
    assign temp3=(p3<4'b0000)?temp2:0;
    add_4_bits_0to4(temp0,temp1);

    assign temp4=(p4<4'b0000)?0:1;
    add_4_bits_0to4(temp2,temp3);

    add_4_bits_0to4(temp4,temp1);

    assign c[0]=p0;
    assign c[1]=p1;
    assign c[2]=p2;
    assign c[3]=p3;
    assign c[4]=p4;
    assign c[5]=p5;
    assign c[6]=p6;
    assign c[7]=p7;
    assign c[8]=p8;
    assign c[9]=p9;
    assign c[10]=p10;
    assign c[11]=p11;
    assign c[12]=p12;
    assign c[13]=p13;
    assign c[14]=p14;
    assign c[15]=p15;
}
```

(Code for 4 bit multiplier)

```
0 = 00000000, 0=00000000, --- = 0000000000000000
100 = 00000000, 0=00000000, --- = 0000000000000000
100 = 00000000, 0=00000000, --- = 0000000000000000
100 = 00000000, 0=00000000, --- = 0000000000000000
100 = 00000000, 0=00000000, --- = 0000000000000000
100 = 00000000, 0=00000000, --- = 0000000000000000
100 = 00000000, 0=00000000, --- = 0000000000000000
```

(c gives the multiplication of a and b)



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Advanced Digital System Design Shirshendu Roy, 2023-09-25 The book is designed to serve as a textbook for courses offered to undergraduate and graduate students enrolled in electrical electronics and communication engineering The objective of this book is to help the readers to understand the concepts of digital system design as well as to motivate the students to pursue research in this field Verilog Hardware Description Language HDL is preferred in this book to realize digital architectures Concepts of Verilog HDL are discussed in a separate chapter and many Verilog codes are given in this book for better understanding Concepts of system Verilog to realize digital hardware are also discussed in a separate chapter The book covers basic topics of digital logic design like binary number systems combinational circuit design sequential circuit design and finite state machine FSM design The book also covers some advanced topics on digital arithmetic like design of high speed adders multipliers dividers square root circuits and CORDIC block The readers can learn about FPGA and ASIC implementation steps and issues that arise at the time of implementation One chapter of the book is dedicated to study the low power design techniques and another to discuss the concepts of static time analysis STA of a digital system Design and implementation of many digital systems are discussed in detail in a separate chapter In the last chapter basics of some advanced FPGA design techniques like partial re configuration and system on chip SoC implementation are discussed These designs can help the readers to design their architecture This book can be very helpful to both undergraduate and postgraduate students and researchers

Gateway to VLSI Bharat Agarwal, Kshitij Goel, 2019-10-04 If you can spare half an hour then we can guarantee success at your next VLSI Very Large Scale Integration FPGA Field Programmable Gate Array STA Static Timing analysis interview Do you want to secure at least 3 to 4 job offers by succeeding at all the phone and on site job interviews for the FPGA DESIGN ENGINEER position Or do you simply want answers for the most frequently asked interview questions in VLSI FPGA digital circuit design Did you know that people who target question answer type preparation for a job interview are 3 4 times more likely to get a job offer than those who don't Did you also know that there is a set of questions that is likely to be repeatedly asked by interviewers across the industry no matter who you talk with in the VLSI FPGA digital design After a total of 17 unsuccessful interviews we thought of writing a book to help upcoming undergrads and experience professionals to get selected in such interviews The book covers every dimension related to FPGA

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Proceedings of World Conference on Artificial Intelligence: Advances and Applications Ashish Kumar

Tripathi, Darpan Anand, Atulya K. Nagar, 2024-09-30 This book is a collection of outstanding research papers presented at the World Conference on Artificial Intelligence Advances and Applications WCAIAA 2024 organized by Sir Padampat Singhania University India and is technically sponsored by Soft Computing Research Society during February 22-23, 2024. The topics covered are agent-based systems, evolutionary algorithms, approximate reasoning, bioinformatics and computational biology, artificial intelligence in modeling and simulation, natural language processing, brain-machine interfaces, collective intelligence, computer vision and speech understanding, data mining, swarm intelligence, machine learning, human-computer interaction, intelligent sensor devices and applications, and intelligent database systems. *Advances in Computing, Communication, Automation and Biomedical Technology* M. G. Sumithra, Arulmurugan Ramu, Chow Chee Onn, 2020-12-30. *Advances in Computing, Communication, Automation and Biomedical Technology* aims to bring together leading academic scientists, researchers, industry representatives, postdoctoral fellows, and research scholars around the world to share their knowledge and research expertise to advance in the areas of Computing, Communication, Electrical, Civil, Mechanical, and Biomedical Systems, as well as to create a prospective collaboration and networking on various areas. It also provides a premier interdisciplinary platform for researchers, practitioners, and educators to present and discuss the most recent innovations, trends, and concerns, as well as practical challenges encountered and solutions adopted in the fields of innovation.

Advances in Multidisciplinary Medical Technologies – Engineering, Modeling and Findings Abdeldjalil

Khelassi, Vania Vieira Estrela, 2020-11-07 This book collects the proceedings of the International Congress on Health Sciences and Medical Technologies ICHSMT held in Tlemcen, Algeria, from December 5 to 7, 2019. The proceedings present a forum for the latest projects and research in scientific and technological development with an emphasis on smart healthcare system design and future technologies. ICHSMT brings together researchers, students, and professionals from the healthcare, corporate, and academic sectors. It includes a far-reaching program supported by a variety of technical tracks that seek to promote medical technologies and innovation at a nationwide level. *Advances in Electrical and Computer Technologies*

Thangaprakash Sengodan, M. Murugappan, Sanjay Misra, 2021-02-26 This book comprises select proceedings of the International Conference on Advances in Electrical and Computer Technologies 2020 (ICAECT 2020). The papers presented in this book are peer-reviewed and cover latest research in electrical, electronics, communication, and computer engineering. Topics covered include smart grids, soft computing techniques in power systems, smart energy management systems, power electronics, feedback control systems, biomedical engineering, geoinformative systems, grid computing, data mining, image and signal processing, video processing, computer vision, pattern recognition, cloud computing, pervasive computing, intelligent systems, artificial intelligence, neural network, and fuzzy logic, broadband communication, mobile, and optical communication.

network security VLSI embedded systems optical networks and wireless communication The volume can be useful for students and researchers working in the different overlapping areas of electrical electronics and communication engineering

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systems wireless communication signal processing power electronics and control theory applications This volume will help researchers and engineers to develop and extend their ideas in upcoming research in electronics and communication

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Proceedings of Third International Conference on Intelligent Computing, Information and Control Systems A. Pasumpon Pandian, Ram Palanisamy, M. Narayanan, Tomonobu Senjyu, 2022-03-14 This book is a collection of papers presented at the International Conference on Intelligent Computing Information and Control Systems ICICCS 2021 It encompasses various research works that help to develop and advance the next generation intelligent computing and control systems The book integrates the computational intelligence and intelligent control systems to provide a powerful

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Jyothi M. Shirur, Rekha P., 2013-07-10 This book is a collection of papers presented by renowned researchers keynote speakers and academicians in the International Conference on VLSI Communication Analog Designs Signals Systems and Networking VCASAN 2013 organized by B N M Institute of Technology Bangalore India during July 17 19 2013 The book provides global trends in cutting edge technologies in electronics and communication engineering The content of the book is useful to engineers researchers and academicians as well as industry professionals

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Table of Contents Vedic Multiplier Verilog Code

1. Understanding the eBook Vedic Multiplier Verilog Code
 - The Rise of Digital Reading Vedic Multiplier Verilog Code
 - Advantages of eBooks Over Traditional Books
2. Identifying Vedic Multiplier Verilog Code
 - Exploring Different Genres
 - Considering Fiction vs. Non-Fiction
 - Determining Your Reading Goals
3. Choosing the Right eBook Platform
 - Popular eBook Platforms
 - Features to Look for in an Vedic Multiplier Verilog Code
 - User-Friendly Interface
4. Exploring eBook Recommendations from Vedic Multiplier Verilog Code
 - Personalized Recommendations
 - Vedic Multiplier Verilog Code User Reviews and Ratings
 - Vedic Multiplier Verilog Code and Bestseller Lists
5. Accessing Vedic Multiplier Verilog Code Free and Paid eBooks
 - Vedic Multiplier Verilog Code Public Domain eBooks
 - Vedic Multiplier Verilog Code eBook Subscription Services
 - Vedic Multiplier Verilog Code Budget-Friendly Options
6. Navigating Vedic Multiplier Verilog Code eBook Formats
 - ePub, PDF, MOBI, and More
 - Vedic Multiplier Verilog Code Compatibility with Devices
 - Vedic Multiplier Verilog Code Enhanced eBook Features
7. Enhancing Your Reading Experience
 - Adjustable Fonts and Text Sizes of Vedic Multiplier Verilog Code
 - Highlighting and Note-Taking Vedic Multiplier Verilog Code
 - Interactive Elements Vedic Multiplier Verilog Code
8. Staying Engaged with Vedic Multiplier Verilog Code

- Joining Online Reading Communities
- Participating in Virtual Book Clubs
- Following Authors and Publishers Vedic Multiplier Verilog Code
- 9. Balancing eBooks and Physical Books Vedic Multiplier Verilog Code
 - Benefits of a Digital Library
 - Creating a Diverse Reading Collection Vedic Multiplier Verilog Code
- 10. Overcoming Reading Challenges
 - Dealing with Digital Eye Strain
 - Minimizing Distractions
 - Managing Screen Time
- 11. Cultivating a Reading Routine Vedic Multiplier Verilog Code
 - Setting Reading Goals Vedic Multiplier Verilog Code
 - Carving Out Dedicated Reading Time
- 12. Sourcing Reliable Information of Vedic Multiplier Verilog Code
 - Fact-Checking eBook Content of Vedic Multiplier Verilog Code
 - Distinguishing Credible Sources
- 13. Promoting Lifelong Learning
 - Utilizing eBooks for Skill Development
 - Exploring Educational eBooks
- 14. Embracing eBook Trends
 - Integration of Multimedia Elements
 - Interactive and Gamified eBooks

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