

Vax Model 121 Manual

Christian G. Meyer



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Alpha Architecture Reference Manual Alpha Architecture Committee, 2014-06-28 This is the authoritative reference on Digital Equipment Corporation's new 64 bit RISC Alpha architecture Written by the designers of the internal Digital specifications this book contains complete descriptions of the common architecture required for all implementations and the interfaces required to support the OSF 1 and OpenVMS operating systems *Alpha Architecture Reference Manual* Richard L. Sites, Alpha Architecture Committee, 1998-04 Alpha Architecture Reference Manual Third Edition is the authoritative reference on the definition of Alpha architecture Revised by the Alpha Architecture Committee this book contains a complete description of the common architecture required of all implementations and describes the interfaces to support the Windows NT Digital UNIX and OpenVMS operating systems The third edition reflects the latest implementations of the architecture including the 21164A 21164PC and 21264 Some of the extensions to the architecture and the enhancement to the technical content include new byte and word load store and sign extend operations new multimedia instructions new population enumeration and floating point square root instructions new instructions to improve data cache efficiency and updated Windows NT section The Alpha chip is the fastest chip on the marketplace today It runs Windows NT UNIX and OpenVMS operating systems New base level server configurations provide four times the memory of current systems Contains updated Windows NT section to reflect current technical port to Alpha Includes new insights into the software aspects of the implementation Covers new multimedia instructions for increased performance with high end graphics applications

Alpha AXP Architecture Reference Manual Richard L. Sites, Richard T. Witek, 2014-05-16 Alpha AXP Architecture Reference Manual Second Edition describes the required behavior of all Alpha implementations as seen by the machine language programmer This book discusses Alpha single board computers which have been introduced to cover the high end embedded controller market Organized into five parts this edition begins with an overview of the instruction set architecture This text then describes the supporting PALcode routines for three operating systems Other parts consider a particular console implementation that is specific to platforms that support the OpenVMS AXP or DEC OSF 1 operating systems This book discusses as well the specific operating system PALcode architecture The final part provides a discussion of console issues for Windows NT with its PALcode description This book is a valuable resource for machine language programmers

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Scientific and Technical Aerospace Reports, 1992

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