SDCC - Small Device C Compiler

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What is SDCC?

SDCC is a retargettable, optimizing ANSI - C compiler suite that targets the Intel MCSSI based microprocessors (8031, 8032, 8051, 8052, etc.), Maxim (formerly Dullas) DSSDC390 variants, Pre-escale (formerly Motoroids) HC0S based (MotS, 808) and ZB0g ZB0 based MCUs (zB0, z180, gbz80, Rabbit 2000/3000, Rabbit 3000A). Work is in progress on supporting the Microchip PIC16 and PSC18 targets. It can be retargeted for other microprocessors.

SDCC suite is a collection of several components derived from different sources with different FOSS licenses. SDCC compiler suite include:

- sdas and sdld, a retargettable assembler and linker, based on ASXXXX, written by Alan Baldwin; (GPL).
- sdepp preprocessor, based on GCC cap; (GPL).
- ucsim simulators, originally written by Daniel Drotos; (GPL).
 sdodb source level debugger, originally written by Sandeep Dutta; (GPL).
- adbinutils library archive utilities, including sdar, sdranib and sdnm, derived from GNU Binutils; (GPL)
- SDCC run-time Bbraries; (GPL+LE). Pic device libraries and header files are derived from Microchip header (.inc) and limiter script (.ibr) files. Microchip requires that "The header files should state that they are only to be used with authentic Microchip devices" which makes them incompatible with the GPL.
- gcc-test regression tests, derived from gcc-testswite; (no license explicitely specified, but since it is a part of GCC is probably GPL licensed)
- packihx; (public domain)
- makebin; (zilb/libpng License)
- sdoc C compiler, originally written by Sandoep Dutta; (GPL). Some of the features include:
 - extensive MCU specific language extensions, allowing effective use of the underlying hardware.
 - a host of standard optimisations such as global sub expression elimination, loop optimisations (loop invariant, strength reduction of induction variables and loop reversing), constant folding and propagation, copy propagation, dead code elimination and jump tables for 'switch' statements.
 - MCU specific optimizations, including a global register allocator.
 - adaptable MCU specific backend that should be well suited for other 8 bit MCUs
 - · independent rule based peep hole optimizer.
 - a full range of data types: char (8 bits, 1 byte), short (16 bits, 2 bytes), lint (16 bits, 2 bytes), long (32 bit, 4 bytes), float (4 byte IEEE) and _Bool/bool;
 basic (no integer constants) support for long long (64 bit, 8 bytes) data types for the z80, z180, r2k, r3ka, gbz80, hc08 and s08 targets.
 - the ability to add inline assembler code anywhere in a function.
 - the ability to report on the complexity of a function to help decide what should be re-written in assembler.
 - . a good selection of automated regression tests.

SDCC was written by Sandeep Dutta and released under a **GPL** license. Since its initial release there have been numerous bug flixes and improvements. As of December 1999, the code was moved to SourceForge where all the "users turned developers" can access the same source tree. SDCC is constantly being updated with all the users' and developers' input.

News

May 20th, 2013: Small Device C Compiler 3.3.0 released.

A new release of SDCC, the portable optimizing compiler for 8051, DS390, Z80, Z180, Rabbit 2000, HC08 and PIC microprocessors is now available (http://sdcc.uouereforge.net). Sources, documentation and binaries compiled for x86 Unux, x86 and x64 MS Windows and x86 and PPC Mac OS X are available.

SDCC 3.3.0 Feature List:

- . Many small improvements in code generation for the z80-related ports merged smallopts branch
- lospre (currently enabled for z80-related and hc08-related ports only) merged lospre branch
- More efficient initialization of globals in x80, x180, r2k and r3ka ports.
- Inclusion of tests from the gcc test suite into the sdcc regression test suite led to many bugs being found and fixed.
- Split sdas390 from sdas8051.
- Merged big parts of ASxxxx v5 into sdas
- . New pic devices (synchronization with MPLABX 1.60). (Except for very old MCU-s.)
- New script which disassembles those hex files, in which MCSS1 code there is: (mcsS1-disasm.pl)
- Added the PIC16F1788 and PIC16F1789 devices.
- C11 _Alignof operator.
- C11 _Alignas alignment specifier.
- C11 _Static_Assert static assertion.

Numerous feature requests and bug fixes are included as well.

You can download the release from: https://sourceforce.net/projects/sdcc/files/

What Platforms are Supported?

Linux - x86, Hicrosoft Windows - x86, Hicrosoft Windows - x86_64 and Hac OS X are the primary, so called "officially supported" platforms.

Small Device C Compiler Manual

Liying Dong

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Hardware Hacking Projects for Geeks Scott Fullam, 2004-01-28 A collection of unusual projects for computer hardware geeks of all ages explains how to create such projects as a personal Lojack system Web enabled coffee machine cubicle intrusion detection systems and a laptop battery extender **Ultrasonic Distance Measurement Using Basys 3 FPGA Board** Ramazan Kahraman, 2019-10-22 System on a Chip SoC technology which has evolved in recent years is developed from different devices A processor several memory and peripheral components are located on a single chip to form today's high performance SoCs with hundreds of IP blocks IP cores are validated design blocks used as part of complex digital designs Those designs are utilizing a hardware description language like VHDL or Verilog In this way time and cost of launching the product are reduced Thanks to SoC the features of computers were able to be reduced to the miniature level Microcontrollers have the features of computer systems on a single chip They are used to collect process and manipulate data in complex projects The complexity of microcontrollers has increased to provide better performance and flexibility to meet customer requirements However it must be able to adapt to operational changes The hardware of a microcontroller can not be changed afterward If subsequent changes are nevertheless necessary these are associated with high additional costs Reconfigurable devices such as FPGAs can reconfigure the hardware to design develop and deploy high performance digital systems With the power of a SoC combined with the flexibility of an FPGA the MC8051 IP Core proves to be a great alternative to purely microcontroller based systems Tools and Algorithms for the Construction and Analysis of **Systems** Erika Abraham, Klaus Havelund, 2014-03-21 This book constitutes the proceedings of the 20th International Conference on Tools and Algorithms for the Construction and Analysis of Systems TACAS 2014 which took place in Grenoble France in April 2014 as part of the European Joint Conferences on Theory and Practice of Software ETAPS 2014 The total of 42 papers included in this volume consisting of 26 research papers 3 case study papers 6 regular tool papers and 7 tool demonstrations papers were carefully reviewed and selected from 161 submissions In addition the book contains one invited contribution The papers are organized in topical sections named decision procedures and their application in analysis complexity and termination analysis modeling and model checking discrete systems timed and hybrid systems monitoring fault detection and identification competition on software verification specifying and checking linear time properties synthesis and learning quantum and probabilistic systems as well as tool demonstrations and case studies Fixing Access Annoyances Phil Mitchell, Evan Callahan, 2006-02-21 When an application is part of the Microsoft Office suite it s sure to be a leader in its field In the realm of desktop database management Access is top dog with millions of users But this is one dog that can bite Although Access is a powerful relational tool with the fetching talents of a Labrador it s not an easy beast to train Still millions of users count on Access for everything from managing parts databases to running Web catalogs to working as a front end to mondo SQL databases But Access is chockablock with annoyances report hassles guery

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