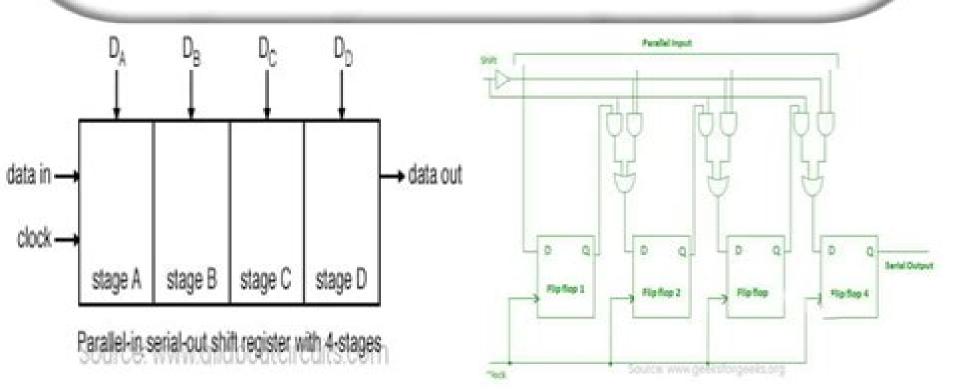
Parallel-In Serial-Out (Piso)

- Parallel-In Serial-Out (PISO) refers to a type of shift register in digital electronics, allowing data loading in parallel but reading it serially
- The main function of PISO is to convert parallel data to serial format, streamlining data transmission in digital systems
- Key components include storage registers for binary data and control signals like shift, load, and clock
- PISO shift registers are vital in digital communications, data conversion, processing, and digital signal processing



Timing Diagram Of Parallel In Serial Out

Tertulien Ndjountche

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Digital Electronics and System Abhishek Bhatt, 2025-06-01 , DIGITAL DESIGN NATARAJAN, R.

ANANDA,2015-01-17 Primarily intended for undergraduate engineering students of Electronics and Communication Electronics and Electrical Electronics and Instrumentation Computer Science and Information Technology this book will also be useful for the students of BCA B Sc Electronics and CS M Sc Electronics and CS and MCA Digital Design is a student friendly textbook for learning digital electronic fundamentals and digital circuit design It is suitable for both traditional design of digital circuits and HDL based digital design This well organised text gives a comprehensive view of Boolean logic logic gates and combinational circuits synchronous and asynchronous circuits memory devices semiconductor devices and PLDs and HDL VHDL and Verilog programming Numerous solved examples are given right after conceptual discussion to provide better comprehension of the subject matter VHDL programs along with simulation results are given for better understanding of VHDL programming Key features Well labelled illustrations provide practical understanding of the concepts GATE level MCQs with answers along with detailed explanation wherever required at the end of each chapter help students to prepare for competitive examinations Short questions with answers and appropriate number of review questions at the end of each chapter are useful for the students to prepare for university exams and competitive exams Separate chapters on VHDL and Verilog programming along with simulated results are included to enhance the programming skills of HDL

Digital Electronics 2 Tertulien Ndjountche, 2016-08-16 As electronic devices become increasingly prevalent in everyday life digital circuits are becoming even more complex and smaller in size This book presents the basic principles of digital electronics in an accessible manner allowing the reader to grasp the principles of combinational and sequential logic and the underlying techniques for the analysis and design of digital circuits Providing a hands on approach this work introduces techniques and methods for establishing logic equations and designing and analyzing digital circuits Each chapter is supplemented with practical examples and well designed exercises with worked solutions This second of three volumes focuses on sequential and arithmetic logic circuits It covers various aspects related to the following topics latch and flip flop binary counters shift registers arithmetic and logic circuits digital integrated circuit technology semiconductor memory programmable logic circuits Along with the two accompanying volumes this book is an indispensable tool for students at a bachelors or masters level seeking to improve their understanding of digital electronics and is detailed enough to serve as a reference for electronic automation and computer engineers <u>Digital Electronics</u> Franco Zappa, 2019-09-11 The book is addressed to an audience interested in the hardware design of digital electronic circuits and systems It introduces the basics of digital electronics and then describes in detail both combinational and sequential logics and components The book aims at providing an in depth overview of the devices and components necessary to design digital electronic systems by exploiting commercially available components The book describes the most important concepts components internal block diagrams

schematics and functional specifications implementations and design tricks that are the fundamental building blocks of any complex electronic system designed to be implemented either through discrete components in electronic boards or by means of single chip programmable logic such as Field Programmable Gate Arrays and microcontrollers The topics covered by the book are Basic and advanced logic gates TTL and CMOS logic families and interoperability Combinational logic and truth table Sum of Products Product of Sums and Karnaugh maps design Sequential logic and classifications Latches and Flip Flops Combinational MSI integrated circuits encoders decoders comparators parity generators and checkers adders ALU multiplexer demultiplexer Sequential MSI integrated circuits latches and flip flops registers shift registers counters Memories ROM RAM SDRAM E2PROM and flash Basics on 8 bit Microcontrollers **Line Communication System Mr.** Ajeet Singh, 2018-05-14 EduGorilla has its own publishing wing producing exam prep books trade books etc The Navy Electricity and Electronics Training Series: Module 13 Introduction To Number Systems And Logic United States. Navy, 2018-09-16 Module 13 Introduction to Number Systems and Logic Circuits presents the fundamental concepts of number systems Boolean algebra and logic circuits all of which pertain to digital computers The Navy Electricity and Electronics Training Series NEETS was developed for use by personnel in many electrical and electronic related Navy ratings Written by and with the advice of senior technicians in these ratings this series provides beginners with fundamental electrical and electronic concepts through self study. The presentation of this series is not oriented to any specific rating structure but is divided into modules containing related information organized into traditional paths of instruction **Digital** Fundamentals and Applications Dr. C.K. Chanda, Sumit Banerjee, 2011-12 Introduction to Digital Electronics, 1/e Betty Lincoln, 2014 Designed to provide a comprehensive and practical insight to the basic concepts of Digital Electronics this book brings together information on theory operational aspects and practical applications of digital circuits in a succinct style that is suitable for undergraduate students Spread across 16 chapters the book walks the student through the first principles and the Karnaugh mapping reduction technique before proceeding to elaborate on the design and implementation of complex digital circuits With ample examples and exercises to reinforce theory and an exclusive chapter allotted for electronic experiments this textbook is an ideal classroom companion for students **Digital Computer Organization** Mr. Rohit Manglik, 2024-03-15 EduGorilla Publication is a trusted name in the education sector committed to empowering learners with high quality study materials and resources Specializing in competitive exams and academic support EduGorilla provides comprehensive and well structured content tailored to meet the needs of students across various streams and levels Data Conversion Handbook Analog Devices Inc. Analog Devices Inc. Engineeri, 2004-12-18 This comprehensive handbook is a one stop engineering reference Covering data converter fundamentals techniques applications and beginning with the basic theoretical elements necessary for a complete understanding of data converters this reference covers all the latest advances in the field This text describes in depth the theory behind and the practical design of data conversion circuits as well as

describing the different architectures used in AD and DA converters Details are provided on the design of high speed ADCs high accuracy DACs and ADCs and sample and hold amplifiers Also this reference covers voltage sources and current reference noise shaping coding and sigma delta converters and much more The book s 900 plus pages are packed with design information and application circuits including guidelines on selecting the most suitable converters for particular applications You ll find the very latest information on Data converter fundamentals such as key specifications noise sampling and testing Architectures and processes including SAR flash pipelined folding and more Practical hardware design techniques for mixed signal systems such as driving ADCs buffering DAC outputs sampling clocks layout interfacing support circuits and tools Data converter applications dealing with precision measurement data acquisition audio display DDS software radio and many more The accompanying CD ROM provides software tools for testing and analyzing data converters as well as a searchable pdf version of the text Brings together a huge amount of information impossible to locate elsewhere Many recent advances in converter technology simply aren t covered in any other book A must have design reference for any electronics design engineer or technician **Sequential Logic** Joseph Cavanagh, 2018-10-03 Until now there was no single resource for actual digital system design Using both basic and advanced concepts Sequential Logic Analysis and Synthesis offers a thorough exposition of the analysis and synthesis of both synchronous and asynchronous sequential machines With 25 years of experience in designing computing equipment the author stresses the practical design of state machines He clearly delineates each step of the structured and rigorous design principles that can be applied to practical applications The book begins by reviewing the analysis of combinatorial logic and Boolean algebra and goes on to define seguential machines and discuss traditional and alternative methods for synthesizing synchronous sequential machines The final chapters deal with asynchronous sequential machines and pulse mode asynchronous sequential machines Because this volume is technology independent these techniques can be used in a variety of fields such as electrical and computer engineering as well as nanotechnology By presenting each method in detail expounding on several corresponding examples and providing over 500 useful figures Sequential Logic is an excellent tutorial on analysis and synthesis procedures Digital Design from Zero to One Jerry D. Daniels, 1996-04-19 Takes a fresh look at basic digital design From definition to example to graphic illustration to simulation result the book progresses through the main themes of digital design Technically up to date this book covers all the latest topics Field programmable gate arrays PALs and ROMs The latest memory chips for SRAM and DRAM are shown Software for creating the excitation equations of FSM are covered as well as LogicWorks and Beige Bag PC Microprocessor System Design Michael J. Spinks, 2013-10-22 Microprocessor System Design A Practical and more Introduction describes the concepts and techniques incorporated into the design of electronic circuits particularly microprocessor boards and their peripherals The book reviews the basic building blocks of the electronic systems composed of digital logic levels gate output circuitry and analog components resistors capacitors diodes transistors. The text also

describes operational amplifiers op amp that use a negative feedback technique to improve the parameters of the op amp The design engineer can use programmable array logic PAL to replace standard discrete TTL and CMOS gates in circuits The PAL is programmable and configurable to match the requirement of a given circuit Using PAL can save space a very important factor in the miniaturization process Examples of PAL applications include the BCD counter the LS 138 emulator and a priority interrupt encoder The book also explains the operation and function of a microprocessor the bus based systems analog to digital conversion and vice versa The text is suitable for programmers computer engineers computer technicians and computer instructors dealing with many aspects of computers such as programming networking engineering or design

VLSI Circuits and Embedded Systems Hafiz Md. Hasan Babu, 2022-07-29 Very Large Scale Integration VLSI creates an integrated circuit IC by combining thousands of transistors into a single chip While designing a circuit reduction of power consumption is a great challenge VLSI designs reduce the size of circuits which eventually reduces the power consumption of the devices However it increases the complexity of the digital system Therefore computer aided design tools are introduced into hardware design processes Unlike the general purpose computer an embedded system is engineered to manage a wide range of processing tasks Single or multiple processing cores manage embedded systems in the form of microcontrollers digital signal processors field programmable gate arrays and application specific integrated circuits Security threats have become a significant issue since most embedded systems lack security even more than personal computers Many embedded systems hacking tools are readily available on the internet Hacking in the PDAs and modems is a pervasive example of embedded systems hacking This book explores the designs of VLSI circuits and embedded systems These two vast topics are divided into four parts In the book s first part the Decision Diagrams DD have been covered DDs have extensively used Computer Aided Design CAD software to synthesize circuits and formal verification The book s second part mainly covers the design architectures of Multiple Valued Logic MVL Circuits MVL circuits offer several potential opportunities to improve present VLSI circuit designs The book s third part deals with Programmable Logic Devices PLD PLDs can be programmed to incorporate a complex logic function within a single IC for VLSI circuits and Embedded Systems The fourth part of the book concentrates on the design architectures of Complex Digital Circuits of Embedded Systems As a whole from this book core researchers academicians and students will get the complete picture of VLSI Circuits and Embedded Systems and their **LED-Based Visible Light Communications** Nan Chi,2018-09-01 The book systematically introduces the applications visible light communication VLC technology in detail Basic concepts and how to realize the system are both illustrated including the transmitter channel and the receiver In addition a good many experimental results are presented to help readers further understand the VLC technologies The upper layer protocols of visible light communication system and the technology trends are also discussed This book can be a good reference work for researchers engineers and graduate students in the fields of communications LED and optics **Digital Systems** Raj Kamal, 2009 Foc 81 East Fiber Optics

& Communications Proceedings Bond,1981 Fiber Optic Computer and Data Links IGIC, Inc. Staff,1994 **DIGITAL ELECTRONICS - II**,2025-03-21 TP SOLVED SERIES For BCA Bachelor of Computer Applications Part II Fourth Semester Rashtrasant Tukadoji Maharaj Nagpur University RTMNU

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