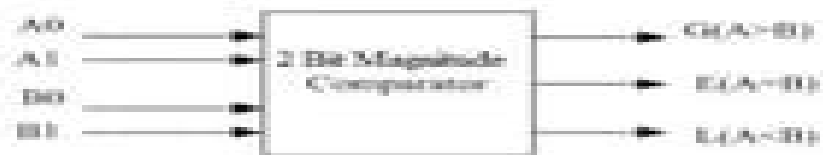


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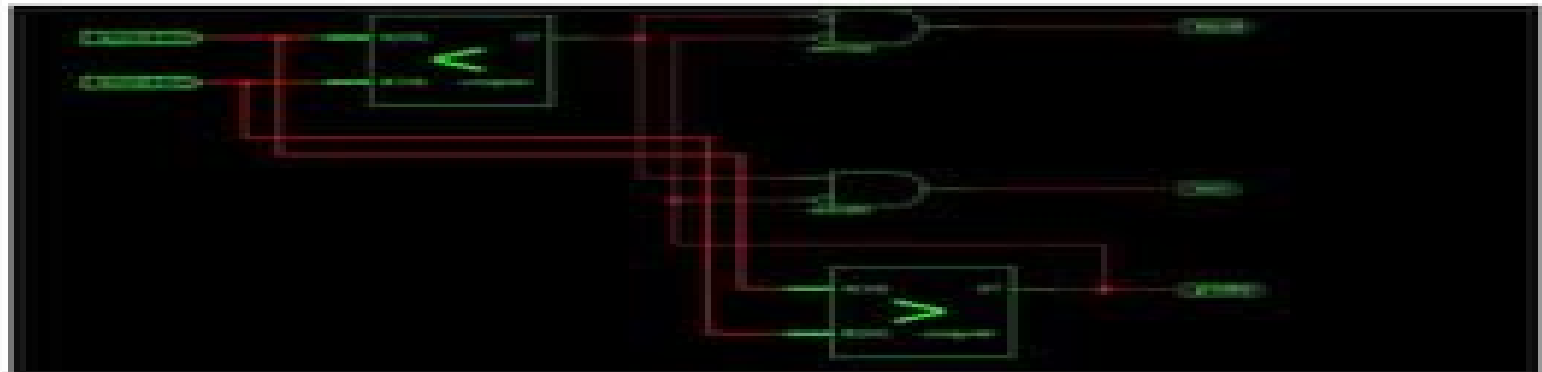
### Block Diagram :



### Truth Table :

| Inputs         | Inputs         | Inputs         | Inputs         | Outputs | Outputs | Outputs |
|----------------|----------------|----------------|----------------|---------|---------|---------|
| A <sub>1</sub> | A <sub>0</sub> | B <sub>1</sub> | B <sub>0</sub> | A < B   | A = B   | A > B   |
| 0              | 0              | 0              | 0              | 0       | 1       | 0       |
| 0              | 0              | 0              | 1              | 1       | 0       | 0       |
| 0              | 0              | 1              | 0              | 1       | 0       | 0       |
| 0              | 0              | 1              | 1              | 1       | 0       | 0       |
| 0              | 1              | 0              | 0              | 0       | 0       | 1       |
| 0              | 1              | 0              | 1              | 0       | 1       | 0       |
| 0              | 1              | 1              | 0              | 1       | 0       | 0       |
| 0              | 1              | 1              | 1              | 1       | 0       | 0       |
| 1              | 0              | 0              | 0              | 0       | 0       | 1       |
| 1              | 0              | 0              | 1              | 0       | 0       | 1       |
| 1              | 0              | 1              | 0              | 0       | 1       | 0       |
| 1              | 0              | 1              | 1              | 0       | 0       | 1       |
| 1              | 1              | 0              | 0              | 0       | 1       | 0       |
| 1              | 1              | 0              | 1              | 0       | 0       | 1       |
| 1              | 1              | 1              | 0              | 0       | 0       | 1       |
| 1              | 1              | 1              | 1              | 0       | 1       | 0       |

### Logic Circuit Diagram :



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Department of Electrical and  
Computer Engineering. Electro-optics  
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