

Chapter – 13

Intel 8086 Microprocessor architecture, features, and signals

Think & Answer type Questions:

1. How much memory in terms of bytes can be interfaced with 8086? Why?
Memory with a capacity of 1 MB can be interfaced with 8086 as it has 20 address lines (since $2^{20} = 1 \text{ MB}$).
2. What is the minimum and maximum size of a segment in terms of bytes? Why?
The minimum size of a segment can be one byte. The maximum size of a segment is 64 KB as the offset address size is 16 bits (since $2^{16} = 64\text{KB}$).
3. Why memory is divided into segments in 8086? What are its advantages?
The 8086 accesses each memory location using the content of a segment register which indicates the base address of a memory segment and an offset register which indicates the offset within the memory segment from where the data or instruction is accessed. Hence the memory is divided into segments.


The memory segmentation allows us to write relocatable program and data, by which the program and data to be executed by the 8086, can be placed anywhere in the memory map of 8086 (i.e. from the address 00000H to FFFFFH), and can be executed without any modification in the program and data. It also leads to good data structure since the program, data and stack are kept in separate segments.

4. How many 8K x 8 memory chips are required to construct 1 MB memory?
1 MB = 2^{20} bytes = $2^{20} \times 8$ bits
Each 8K x 8 memory chip can store $2^{13} \times 8$ bits.
Therefore, Number of 8K x 8 memory chips needed = $(2^{20} \times 8) / (2^{13} \times 8) = 2^7 = 128$
5. Which pin of 8086 determines mode of operation? How?
The $\overline{MN}/\overline{MX}$ pin of 8086 determines the mode of operation of 8086. If it is connected to +5V, 8086 operates in minimum mode and if it is connected to 0V (i.e. Ground), 8086 operates in maximum mode.
6. What are the differences between NMI and INTR interrupts in 8086?
The NMI interrupt is a non-maskable interrupt and it can not be disabled by software. The NMI interrupt has higher priority over INTR interrupt. The NMI interrupt is a vectored interrupt as it has a specific interrupt type number allotted to it, which is type 02H.

The INTR interrupt is a maskable interrupt and it can be enabled or disabled by setting or clearing the 'I' flag in the flag register respectively. The INTR interrupt has lower priority over NMI interrupt. The INTR interrupt is a non-vectored interrupt as it has no specific interrupt type number allotted to it by default.

Solutions Manual Microprocessor Architecture

Patrick H. Stakem



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