

# VERILOG PROJECT

## VEDIC MULTIPLIER ✕



### 8-Bit Multiplier

```
module veda_8bits_8x8 {
    input [7:0]a;
    input [7:0]b;
    output [15:0]c;

    wire [15:0]p0;
    wire [15:0]p1;
    wire [15:0]p2;
    wire [15:0]p3;
    wire [15:0]p4;
    wire [15:0]p5;
    wire [15:0]p6;
    wire [15:0]p7;
    wire [15:0]p8;
    wire [15:0]p9;
    wire [15:0]p10;
    wire [15:0]p11;
    wire [15:0]p12;
    wire [15:0]p13;
    wire [15:0]p14;
    wire [15:0]p15;

    veda_8x8_0 a,b,p0,p1,p2,p3,p4,p5,p6,p7,p8,p9,p10,p11,p12,p13,p14,p15;
    veda_8x8_1 a,b,p1,p2,p3,p4,p5,p6,p7,p8,p9,p10,p11,p12,p13,p14,p15;
    veda_8x8_2 a,b,p2,p3,p4,p5,p6,p7,p8,p9,p10,p11,p12,p13,p14,p15;
    veda_8x8_3 a,b,p3,p4,p5,p6,p7,p8,p9,p10,p11,p12,p13,p14,p15;

    assign p0=a[0]&b[0];
    assign p1=a[0]&b[1];
    assign p2=a[0]&b[2];
    assign p3=a[0]&b[3];
    assign p4=a[0]&b[4];
    assign p5=a[0]&b[5];
    assign p6=a[0]&b[6];
    assign p7=a[0]&b[7];
    assign p8=a[1]&b[0];
    assign p9=a[1]&b[1];
    assign p10=a[1]&b[2];
    assign p11=a[1]&b[3];
    assign p12=a[1]&b[4];
    assign p13=a[1]&b[5];
    assign p14=a[1]&b[6];
    assign p15=a[1]&b[7];

    add_16_0 p0,p1,p2,p3,p4,p5,p6,p7,p8,p9,p10,p11,p12,p13,p14,p15;

    assign c[0]=p0;
    assign c[1]=p1;
    assign c[2]=p2;
    assign c[3]=p3;
    assign c[4]=p4;
    assign c[5]=p5;
    assign c[6]=p6;
    assign c[7]=p7;
    assign c[8]=p8;
    assign c[9]=p9;
    assign c[10]=p10;
    assign c[11]=p11;
    assign c[12]=p12;
    assign c[13]=p13;
    assign c[14]=p14;
    assign c[15]=p15;
}
```

(Code for 8 bit multiplier)

### 4-Bit Multiplier

```
module veda_4x4_4bits_4x4 {
    input [3:0]a;
    input [3:0]b;
    output [7:0]c;

    wire [7:0]p0;
    wire [7:0]p1;
    wire [7:0]p2;
    wire [7:0]p3;
    wire [7:0]p4;
    wire [7:0]p5;
    wire [7:0]p6;
    wire [7:0]p7;

    veda_4x4_0 a,b,p0,p1,p2,p3,p4,p5,p6,p7;
    veda_4x4_1 a,b,p1,p2,p3,p4,p5,p6,p7;
    veda_4x4_2 a,b,p2,p3,p4,p5,p6,p7;
    veda_4x4_3 a,b,p3,p4,p5,p6,p7;

    assign p0=a[0]&b[0];
    assign p1=a[0]&b[1];
    assign p2=a[0]&b[2];
    assign p3=a[0]&b[3];
    assign p4=a[1]&b[0];
    assign p5=a[1]&b[1];
    assign p6=a[1]&b[2];
    assign p7=a[1]&b[3];

    add_8_0 p0,p1,p2,p3,p4,p5,p6,p7;

    assign c[0]=p0;
    assign c[1]=p1;
    assign c[2]=p2;
    assign c[3]=p3;
    assign c[4]=p4;
    assign c[5]=p5;
    assign c[6]=p6;
    assign c[7]=p7;
}
```

(Code for 4 bit multiplier)

```
0 = 00000000, 0=00000000, --- = 0000000000000000
100 = 00000000, 0=00000000, --- = 0000000000000000
100 = 00000000, 0=00000000, --- = 0000000000000000
100 = 00000000, 0=00000000, --- = 0000000000000000
100 = 00000000, 0=00000000, --- = 0000000000000000
100 = 00000000, 0=00000000, --- = 0000000000000000
```

(c gives the multiplication of a and b)



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