

VERILOG PROJECT

VEDIC MULTIPLIER ✕



8-Bit Multiplier

```
module veda_8bits_8x8 {
    input [7:0]a;
    input [7:0]b;
    output [15:0]c;

    wire [15:0]p0;
    wire [15:0]p1;
    wire [15:0]p2;
    wire [15:0]p3;
    wire [15:0]p4;
    wire [15:0]p5;
    wire [15:0]p6;
    wire [15:0]p7;
    wire [15:0]p8;
    wire [15:0]p9;
    wire [15:0]p10;
    wire [15:0]p11;
    wire [15:0]p12;
    wire [15:0]p13;
    wire [15:0]p14;
    wire [15:0]p15;

    veda_8x8_0 a,b,p0,p1,p2,p3,p4,p5,p6,p7,p8,p9,p10,p11,p12,p13,p14,p15;
    veda_8x8_1 a,b,p1,p2,p3,p4,p5,p6,p7,p8,p9,p10,p11,p12,p13,p14,p15;
    veda_8x8_2 a,b,p2,p3,p4,p5,p6,p7,p8,p9,p10,p11,p12,p13,p14,p15;
    veda_8x8_3 a,b,p3,p4,p5,p6,p7,p8,p9,p10,p11,p12,p13,p14,p15;

    assign p0=a[0]&b[0];
    add_8_bits c[0],c[1],p0,p1;
    assign p1=a[0]&b[1];
    assign p2=a[0]&b[2];
    assign p3=a[0]&b[3];
    add_16_bits c[2],c[3],p2,p3;
    assign p4=a[0]&b[4];
    assign p5=a[0]&b[5];
    assign p6=a[0]&b[6];
    assign p7=a[0]&b[7];
    add_16_bits c[4],c[5],p4,p5;
    add_16_bits c[6],c[7],p6,p7;

    assign c[8]=a[1]&b[0];
    add_8_bits c[8],c[9],p1,p0;
    assign c[10]=a[1]&b[1];
    add_8_bits c[10],c[11],p2,p1;
    assign c[12]=a[1]&b[2];
    add_8_bits c[12],c[13],p3,p2;
    assign c[14]=a[1]&b[3];
    add_8_bits c[14],c[15],p4,p3;

    assign c[16]=a[1]&b[4];
    add_8_bits c[16],c[17],p5,p4;
    assign c[18]=a[1]&b[5];
    add_8_bits c[18],c[19],p6,p5;
    assign c[20]=a[1]&b[6];
    add_8_bits c[20],c[21],p7,p6;
    assign c[22]=a[1]&b[7];
    add_8_bits c[22],c[23],p8,p7;

    assign c[24]=a[2]&b[0];
    add_8_bits c[24],c[25],p9,p0;
    assign c[26]=a[2]&b[1];
    add_8_bits c[26],c[27],p10,p1;
    assign c[28]=a[2]&b[2];
    add_8_bits c[28],c[29],p11,p2;
    assign c[30]=a[2]&b[3];
    add_8_bits c[30],c[31],p12,p3;

    assign c[32]=a[2]&b[4];
    add_8_bits c[32],c[33],p13,p4;
    assign c[34]=a[2]&b[5];
    add_8_bits c[34],c[35],p14,p5;
    assign c[36]=a[2]&b[6];
    add_8_bits c[36],c[37],p15,p6;
    assign c[38]=a[2]&b[7];
    add_8_bits c[38],c[39],p16,p7;

    assign c[40]=a[3]&b[0];
    add_8_bits c[40],c[41],p17,p0;
    assign c[42]=a[3]&b[1];
    add_8_bits c[42],c[43],p18,p1;
    assign c[44]=a[3]&b[2];
    add_8_bits c[44],c[45],p19,p2;
    assign c[46]=a[3]&b[3];
    add_8_bits c[46],c[47],p20,p3;

    assign c[48]=a[3]&b[4];
    add_8_bits c[48],c[49],p21,p4;
    assign c[50]=a[3]&b[5];
    add_8_bits c[50],c[51],p22,p5;
    assign c[52]=a[3]&b[6];
    add_8_bits c[52],c[53],p23,p6;
    assign c[54]=a[3]&b[7];
    add_8_bits c[54],c[55],p24,p7;

    assign c[56]=a[4]&b[0];
    add_8_bits c[56],c[57],p25,p0;
    assign c[58]=a[4]&b[1];
    add_8_bits c[58],c[59],p26,p1;
    assign c[60]=a[4]&b[2];
    add_8_bits c[60],c[61],p27,p2;
    assign c[62]=a[4]&b[3];
    add_8_bits c[62],c[63],p28,p3;

    assign c[64]=a[4]&b[4];
    add_8_bits c[64],c[65],p29,p4;
    assign c[66]=a[4]&b[5];
    add_8_bits c[66],c[67],p30,p5;
    assign c[68]=a[4]&b[6];
    add_8_bits c[68],c[69],p31,p6;
    assign c[70]=a[4]&b[7];
    add_8_bits c[70],c[71],p32,p7;

    assign c[72]=a[5]&b[0];
    add_8_bits c[72],c[73],p33,p0;
    assign c[74]=a[5]&b[1];
    add_8_bits c[74],c[75],p34,p1;
    assign c[76]=a[5]&b[2];
    add_8_bits c[76],c[77],p35,p2;
    assign c[78]=a[5]&b[3];
    add_8_bits c[78],c[79],p36,p3;

    assign c[80]=a[5]&b[4];
    add_8_bits c[80],c[81],p37,p4;
    assign c[82]=a[5]&b[5];
    add_8_bits c[82],c[83],p38,p5;
    assign c[84]=a[5]&b[6];
    add_8_bits c[84],c[85],p39,p6;
    assign c[86]=a[5]&b[7];
    add_8_bits c[86],c[87],p40,p7;

    assign c[88]=a[6]&b[0];
    add_8_bits c[88],c[89],p41,p0;
    assign c[90]=a[6]&b[1];
    add_8_bits c[90],c[91],p42,p1;
    assign c[92]=a[6]&b[2];
    add_8_bits c[92],c[93],p43,p2;
    assign c[94]=a[6]&b[3];
    add_8_bits c[94],c[95],p44,p3;

    assign c[96]=a[6]&b[4];
    add_8_bits c[96],c[97],p45,p4;
    assign c[98]=a[6]&b[5];
    add_8_bits c[98],c[99],p46,p5;
    assign c[100]=a[6]&b[6];
    add_8_bits c[100],c[101],p47,p6;
    assign c[102]=a[6]&b[7];
    add_8_bits c[102],c[103],p48,p7;

    assign c[104]=a[7]&b[0];
    add_8_bits c[104],c[105],p49,p0;
    assign c[106]=a[7]&b[1];
    add_8_bits c[106],c[107],p50,p1;
    assign c[108]=a[7]&b[2];
    add_8_bits c[108],c[109],p51,p2;
    assign c[110]=a[7]&b[3];
    add_8_bits c[110],c[111],p52,p3;

    assign c[112]=a[7]&b[4];
    add_8_bits c[112],c[113],p53,p4;
    assign c[114]=a[7]&b[5];
    add_8_bits c[114],c[115],p54,p5;
    assign c[116]=a[7]&b[6];
    add_8_bits c[116],c[117],p55,p6;
    assign c[118]=a[7]&b[7];
    add_8_bits c[118],c[119],p56,p7;

    assign c[120]=a[8]&b[0];
    add_8_bits c[120],c[121],p57,p0;
    assign c[122]=a[8]&b[1];
    add_8_bits c[122],c[123],p58,p1;
    assign c[124]=a[8]&b[2];
    add_8_bits c[124],c[125],p59,p2;
    assign c[126]=a[8]&b[3];
    add_8_bits c[126],c[127],p60,p3;

    assign c[128]=a[8]&b[4];
    add_8_bits c[128],c[129],p61,p4;
    assign c[130]=a[8]&b[5];
    add_8_bits c[130],c[131],p62,p5;
    assign c[132]=a[8]&b[6];
    add_8_bits c[132],c[133],p63,p6;
    assign c[134]=a[8]&b[7];
    add_8_bits c[134],c[135],p64,p7;

    assign c[136]=a[9]&b[0];
    add_8_bits c[136],c[137],p65,p0;
    assign c[138]=a[9]&b[1];
    add_8_bits c[138],c[139],p66,p1;
    assign c[140]=a[9]&b[2];
    add_8_bits c[140],c[141],p67,p2;
    assign c[142]=a[9]&b[3];
    add_8_bits c[142],c[143],p68,p3;

    assign c[144]=a[9]&b[4];
    add_8_bits c[144],c[145],p69,p4;
    assign c[146]=a[9]&b[5];
    add_8_bits c[146],c[147],p70,p5;
    assign c[148]=a[9]&b[6];
    add_8_bits c[148],c[149],p71,p6;
    assign c[150]=a[9]&b[7];
    add_8_bits c[150],c[151],p72,p7;

    assign c[152]=a[10]&b[0];
    add_8_bits c[152],c[153],p73,p0;
    assign c[154]=a[10]&b[1];
    add_8_bits c[154],c[155],p74,p1;
    assign c[156]=a[10]&b[2];
    add_8_bits c[156],c[157],p75,p2;
    assign c[158]=a[10]&b[3];
    add_8_bits c[158],c[159],p76,p3;

    assign c[160]=a[10]&b[4];
    add_8_bits c[160],c[161],p77,p4;
    assign c[162]=a[10]&b[5];
    add_8_bits c[162],c[163],p78,p5;
    assign c[164]=a[10]&b[6];
    add_8_bits c[164],c[165],p79,p6;
    assign c[166]=a[10]&b[7];
    add_8_bits c[166],c[167],p80,p7;

    assign c[168]=a[11]&b[0];
    add_8_bits c[168],c[169],p81,p0;
    assign c[170]=a[11]&b[1];
    add_8_bits c[170],c[171],p82,p1;
    assign c[172]=a[11]&b[2];
    add_8_bits c[172],c[173],p83,p2;
    assign c[174]=a[11]&b[3];
    add_8_bits c[174],c[175],p84,p3;

    assign c[176]=a[11]&b[4];
    add_8_bits c[176],c[177],p85,p4;
    assign c[178]=a[11]&b[5];
    add_8_bits c[178],c[179],p86,p5;
    assign c[180]=a[11]&b[6];
    add_8_bits c[180],c[181],p87,p6;
    assign c[182]=a[11]&b[7];
    add_8_bits c[182],c[183],p88,p7;

    assign c[184]=a[12]&b[0];
    add_8_bits c[184],c[185],p89,p0;
    assign c[186]=a[12]&b[1];
    add_8_bits c[186],c[187],p90,p1;
    assign c[188]=a[12]&b[2];
    add_8_bits c[188],c[189],p91,p2;
    assign c[190]=a[12]&b[3];
    add_8_bits c[190],c[191],p92,p3;

    assign c[192]=a[12]&b[4];
    add_8_bits c[192],c[193],p93,p4;
    assign c[194]=a[12]&b[5];
    add_8_bits c[194],c[195],p94,p5;
    assign c[196]=a[12]&b[6];
    add_8_bits c[196],c[197],p95,p6;
    assign c[198]=a[12]&b[7];
    add_8_bits c[198],c[199],p96,p7;

    assign c[200]=a[13]&b[0];
    add_8_bits c[200],c[201],p97,p0;
    assign c[202]=a[13]&b[1];
    add_8_bits c[202],c[203],p98,p1;
    assign c[204]=a[13]&b[2];
    add_8_bits c[204],c[205],p99,p2;
    assign c[206]=a[13]&b[3];
    add_8_bits c[206],c[207],p100,p3;

    assign c[208]=a[13]&b[4];
    add_8_bits c[208],c[209],p101,p4;
    assign c[210]=a[13]&b[5];
    add_8_bits c[210],c[211],p102,p5;
    assign c[212]=a[13]&b[6];
    add_8_bits c[212],c[213],p103,p6;
    assign c[214]=a[13]&b[7];
    add_8_bits c[214],c[215],p104,p7;

    assign c[216]=a[14]&b[0];
    add_8_bits c[216],c[217],p105,p0;
    assign c[218]=a[14]&b[1];
    add_8_bits c[218],c[219],p106,p1;
    assign c[220]=a[14]&b[2];
    add_8_bits c[220],c[221],p107,p2;
    assign c[222]=a[14]&b[3];
    add_8_bits c[222],c[223],p108,p3;

    assign c[224]=a[14]&b[4];
    add_8_bits c[224],c[225],p109,p4;
    assign c[226]=a[14]&b[5];
    add_8_bits c[226],c[227],p110,p5;
    assign c[228]=a[14]&b[6];
    add_8_bits c[228],c[229],p111,p6;
    assign c[230]=a[14]&b[7];
    add_8_bits c[230],c[231],p112,p7;

    assign c[232]=a[15]&b[0];
    add_8_bits c[232],c[233],p113,p0;
    assign c[234]=a[15]&b[1];
    add_8_bits c[234],c[235],p114,p1;
    assign c[236]=a[15]&b[2];
    add_8_bits c[236],c[237],p115,p2;
    assign c[238]=a[15]&b[3];
    add_8_bits c[238],c[239],p116,p3;

    assign c[240]=a[15]&b[4];
    add_8_bits c[240],c[241],p117,p4;
    assign c[242]=a[15]&b[5];
    add_8_bits c[242],c[243],p118,p5;
    assign c[244]=a[15]&b[6];
    add_8_bits c[244],c[245],p119,p6;
    assign c[246]=a[15]&b[7];
    add_8_bits c[246],c[247],p120,p7;

    assign c[248]=a[16]&b[0];
    add_8_bits c[248],c[249],p121,p0;
    assign c[250]=a[16]&b[1];
    add_8_bits c[250],c[251],p122,p1;
    assign c[252]=a[16]&b[2];
    add_8_bits c[252],c[253],p123,p2;
    assign c[254]=a[16]&b[3];
    add_8_bits c[254],c[255],p124,p3;

    assign c[256]=a[16]&b[4];
    add_8_bits c[256],c[257],p125,p4;
    assign c[258]=a[16]&b[5];
    add_8_bits c[258],c[259],p126,p5;
    assign c[260]=a[16]&b[6];
    add_8_bits c[260],c[261],p127,p6;
    assign c[262]=a[16]&b[7];
    add_8_bits c[262],c[263],p128,p7;

    assign c[264]=a[17]&b[0];
    add_8_bits c[264],c[265],p129,p0;
    assign c[266]=a[17]&b[1];
    add_8_bits c[266],c[267],p130,p1;
    assign c[268]=a[17]&b[2];
    add_8_bits c[268],c[269],p131,p2;
    assign c[270]=a[17]&b[3];
    add_8_bits c[270],c[271],p132,p3;

    assign c[272]=a[17]&b[4];
    add_8_bits c[272],c[273],p133,p4;
    assign c[274]=a[17]&b[5];
    add_8_bits c[274],c[275],p134,p5;
    assign c[276]=a[17]&b[6];
    add_8_bits c[276],c[277],p135,p6;
    assign c[278]=a[17]&b[7];
    add_8_bits c[278],c[279],p136,p7;

    assign c[280]=a[18]&b[0];
    add_8_bits c[280],c[281],p137,p0;
    assign c[282]=a[18]&b[1];
    add_8_bits c[282],c[283],p138,p1;
    assign c[284]=a[18]&b[2];
    add_8_bits c[284],c[285],p139,p2;
    assign c[286]=a[18]&b[3];
    add_8_bits c[286],c[287],p140,p3;

    assign c[288]=a[18]&b[4];
    add_8_bits c[288],c[289],p141,p4;
    assign c[290]=a[18]&b[5];
    add_8_bits c[290],c[291],p142,p5;
    assign c[292]=a[18]&b[6];
    add_8_bits c[292],c[293],p143,p6;
    assign c[294]=a[18]&b[7];
    add_8_bits c[294],c[295],p144,p7;

    assign c[296]=a[19]&b[0];
    add_8_bits c[296],c[297],p145,p0;
    assign c[298]=a[19]&b[1];
    add_8_bits c[298],c[299],p146,p1;
    assign c[300]=a[19]&b[2];
    add_8_bits c[300],c[301],p147,p2;
    assign c[302]=a[19]&b[3];
    add_8_bits c[302],c[303],p148,p3;

    assign c[304]=a[19]&b[4];
    add_8_bits c[304],c[305],p149,p4;
    assign c[306]=a[19]&b[5];
    add_8_bits c[306],c[307],p150,p5;
    assign c[308]=a[19]&b[6];
    add_8_bits c[308],c[309],p151,p6;
    assign c[310]=a[19]&b[7];
    add_8_bits c[310],c[311],p152,p7;

    assign c[312]=a[20]&b[0];
    add_8_bits c[312],c[313],p153,p0;
    assign c[314]=a[20]&b[1];
    add_8_bits c[314],c[315],p154,p1;
    assign c[316]=a[20]&b[2];
    add_8_bits c[316],c[317],p155,p2;
    assign c[318]=a[20]&b[3];
    add_8_bits c[318],c[319],p156,p3;

    assign c[320]=a[20]&b[4];
    add_8_bits c[320],c[321],p157,p4;
    assign c[322]=a[20]&b[5];
    add_8_bits c[322],c[323],p158,p5;
    assign c[324]=a[20]&b[6];
    add_8_bits c[324],c[325],p159,p6;
    assign c[326]=a[20]&b[7];
    add_8_bits c[326],c[327],p160,p7;

    assign c[328]=a[21]&b[0];
    add_8_bits c[328],c[329],p161,p0;
    assign c[330]=a[21]&b[1];
    add_8_bits c[330],c[331],p162,p1;
    assign c[332]=a[21]&b[2];
    add_8_bits c[332],c[333],p163,p2;
    assign c[334]=a[21]&b[3];
    add_8_bits c[334],c[335],p164,p3;

    assign c[336]=a[21]&b[4];
    add_8_bits c[336],c[337],p165,p4;
    assign c[338]=a[21]&b[5];
    add_8_bits c[338],c[339],p166,p5;
    assign c[340]=a[21]&b[6];
    add_8_bits c[340],c[341],p167,p6;
    assign c[342]=a[21]&b[7];
    add_8_bits c[342],c[343],p168,p7;

    assign c[344]=a[22]&b[0];
    add_8_bits c[344],c[345],p169,p0;
    assign c[346]=a[22]&b[1];
    add_8_bits c[346],c[347],p170,p1;
    assign c[348]=a[22]&b[2];
    add_8_bits c[348],c[349],p171,p2;
    assign c[350]=a[22]&b[3];
    add_8_bits c[350],c[351],p172,p3;

    assign c[352]=a[22]&b[4];
    add_8_bits c[352],c[353],p173,p4;
    assign c[354]=a[22]&b[5];
    add_8_bits c[354],c[355],p174,p5;
    assign c[356]=a[22]&b[6];
    add_8_bits c[356],c[357],p175,p6;
    assign c[358]=a[22]&b[7];
    add_8_bits c[358],c[359],p176,p7;

    assign c[360]=a[23]&b[0];
    add_8_bits c[360],c[361],p177,p0;
    assign c[362]=a[23]&b[1];
    add_8_bits c[362],c[363],p178,p1;
    assign c[364]=a[23]&b[2];
    add_8_bits c[364],c[365],p179,p2;
    assign c[366]=a[23]&b[3];
    add_8_bits c[366],c[367],p180,p3;

    assign c[368]=a[23]&b[4];
    add_8_bits c[368],c[369],p181,p4;
    assign c[370]=a[23]&b[5];
    add_8_bits c[370],c[371],p182,p5;
    assign c[372]=a[23]&b[6];
    add_8_bits c[372],c[373],p183,p6;
    assign c[374]=a[23]&b[7];
    add_8_bits c[374],c[375],p184,p7;

    assign c[376]=a[24]&b[0];
    add_8_bits c[376],c[377],p185,p0;
    assign c[378]=a[24]&b[1];
    add_8_bits c[378],c[379],p186,p1;
    assign c[380]=a[24]&b[2];
    add_8_bits c[380],c[381],p187,p2;
    assign c[382]=a[24]&b[3];
    add_8_bits c[382],c[383],p188,p3;

    assign c[384]=a[24]&b[4];
    add_8_bits c[384],c[385],p189,p4;
    assign c[386]=a[24]&b[5];
    add_8_bits c[386],c[387],p190,p5;
    assign c[388]=a[24]&b[6];
    add_8_bits c[388],c[389],p191,p6;
    assign c[390]=a[24]&b[7];
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    assign c[398]=a[25]&b[3];
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    assign c[400]=a[25]&b[4];
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    assign c[402]=a[25]&b[5];
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    assign c[406]=a[25]&b[7];
    add_8_bits c[406],c[407],p200,p7;

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    add_8_bits c[410],c[411],p202,p1;
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    add_8_bits c[412],c[413],p203,p2;
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    add_8_bits c[416],c[417],p205,p4;
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    assign c[420]=a[26]&b[6];
    add_8_bits c[420],c[421],p207,p6;
    assign c[422]=a[26]&b[7];
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    add_8_bits c[428],c[429],p211,p2;
    assign c[430]=a[27]&b[3];
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    assign c[432]=a[27]&b[4];
    add_8_bits c[432],c[433],p213,p4;
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    add_8_bits c[506],c[507],p250,p1;
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    add_8_bits c[508],c[509],p251,p2;
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    assign c[542]=a[34]&b[3];
    add_8_bits c[542],c[543],p268,p3;

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    add_8_bits c[544],c[545],p269,p4;
    assign c[546]=a[34]&b[5];
    add_8_bits c[546],c[547],p270,p5;
    assign c[548]=a[3
```

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**M. G. Sumithra ,Arulmurugan Ramu
,Chow Chee Onn**

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