

Malfunction Code		Detected Item	Detected Failure Condition	Check For
C00		NO FAULT	—	—
C11		CMP sensor	The signal does not reach ECM for 3 sec. or more, after receiving the starter signal.	CMP sensor wiring and mechanical parts CMP sensor, intake cam pin, wiring/coupler connection
P0340				
C12		CKP sensor	The signal does not reach ECM for 3 sec. or more, after receiving the starter signal.	CKP sensor wiring and mechanical parts CKP sensor, lead wire/coupler connection
P0335				
C13		IAP sensor	The sensor should produce following voltage. $0.5\text{ V} \leq \text{Sensor voltage} < 4.85\text{ V}$ In other than the above range, C13 (P0105) is indicated.	IAP sensor, lead wire/coupler connection
P0105	H		Sensor voltage is higher than specified value.	IAP sensor circuit shorted to VCC or ground circuit open
	L		Sensor voltage is lower than specified value.	IAP sensor circuit open or shorted to ground or VCC circuit open
C14		TP sensor	The sensor should produce following voltage. $0.2\text{ V} \leq \text{Sensor voltage} < 4.8\text{ V}$ In other than the above range, C14 (P0120) is indicated.	TP sensor, lead wire/coupler connection
P0120	H		Sensor voltage is higher than specified value.	TP sensor circuit shorted to VCC or ground circuit open
	L		Sensor voltage is lower than specified value.	TP sensor circuit open or shorted to ground or VCC circuit open
C15		ECT sensor	The sensor voltage should be the following. $0.15\text{ V} \leq \text{Sensor voltage} < 4.85\text{ V}$ In other than the above range, C15 (P0115) is indicated.	ECT sensor, lead wire/coupler connection
P0115	H		Sensor voltage is higher than specified value.	ECT sensor circuit open or ground circuit open
	L		Sensor voltage is lower than specified value.	ECT sensor circuit shorted to ground
C21		IAT sensor	The sensor voltage should be the following. $0.15\text{ V} \leq \text{Sensor voltage} < 4.85\text{ V}$ In other than the above range, C21 (P0110) is indicated.	IAT sensor, lead wire/coupler connection
P0110	H		Sensor voltage is higher than specified value.	IAT sensor circuit open or ground circuit open
	L		Sensor voltage is lower than specified value.	IAT sensor circuit shorted to ground
C22		AP sensor	The sensor voltage should be the following. $0.5\text{ V} \leq \text{Sensor voltage} < 4.85\text{ V}$ In other than the above range, C22 (P1450) is indicated.	AP sensor, lead wire/coupler connection
P1450	H		Sensor voltage is higher than specified value.	AP sensor circuit shorted to VCC or ground circuit open
	L		Sensor voltage is lower than specified value.	AP sensor circuit open or shorted to ground or VCC circuit open

Suzuki Fault Code Manual

Yi-Tong Ma

A red circular graphic with a gradient, appearing as a stylized arrow or a partial circle, located to the right of the author's name.

Suzuki Fault Code Manual:

Chilton's Import Auto Service Manual ,2000 Contains general information for technicians on the specifications MIL resetting and DTC retrieval accessory drive belts timing belts brakes oxygen sensors electric cooling fans and heater cores of twenty one types of import cars Engine Code Manual Chilton Automotive Books,The Nichols/Chilton,Chilton,1995 The complete manual for understanding engine codes troubleshooting basic maintenance and more VW Polo Petrol & Diesel Service & Repair Manual R. M. Jex,2007 Hatchback including special limited editions Does NOT cover features specific to Dune models or facelifted Polo range introduced June 2005 Petrol 1 2 litre 1198cc 3 cyl 1 4 litre 1390cc non FSI 4 cyl Does NOT cover 1 4 litre FSI engines Diesel 1 4 litre 1422cc 3 cyl 1 9 litre 1896cc 4 cyl inc PD TDI turbo Energy Research Abstracts ,1993 *Technical Publications by JAERI Staff in ...* Nihon Genshiryoku Kenkyujo,1993 *Information and Communications Security* Lucas C. K. Hui,S. H. Qing,Elaine Shi,S. M. Yiu,2015-08-01 This book constitutes the thoroughly refereed post conference proceedings of the 16th International Conference on Information and Communications Security ICISC 2014 held in Hong Kong China in December 2014 The 22 revised full papers including two invited talks presented were carefully selected from 90 submissions The papers provide the latest results in research development and applications in the field of information security and cryptology **Official Gazette of the United States Patent and Trademark Office** ,2002 **Computer Safety, Reliability, and Security** Andrea Bondavalli,Felicita Di Giandomenico,2014-09-03 This book constitutes the refereed proceedings of the 33nd International Conference on Computer Safety Reliability and Security SAFECOMP 2014 held in Florence Italy in September 2014 The 20 revised full papers presented together with 3 practical experience reports were carefully reviewed and selected from 85 submissions The papers are organized in topical sections on fault injection techniques verification and validation techniques automotive systems coverage models and mitigation techniques assurance cases and arguments system analysis security and trust notations languages for safety related aspects safety and security **Advances in Logic Based Intelligent Systems** Kazumi Nakamatsu,Jair Minoro Abe,2005 LAPTEC 2005 promoted the discussion and interaction between researchers and practitioners focused on both theoretical and practical disciplines concerning logics applied to technology with diverse backgrounds including all kinds of intelligent systems having classical or non classical logics as underlying common matters **Scientific and Technical Aerospace Reports** ,1987 **Decidability of Parameterized Verification** Roderick Bloem,Swen Jacobs,Ayrat Kalimov,Igor Konnov,2022-05-31 While the classic model checking problem is to decide whether a finite system satisfies a specification the goal of parameterized model checking is to decide given finite systems n parameterized by $n \in \mathbb{N}$ whether for all $n \in \mathbb{N}$ the system n satisfies a specification In this book we consider the important case of n being a concurrent system where the number of replicated processes depends on the parameter n but each process is independent of n Examples are cache coherence protocols networks of finite state agents and systems that solve mutual exclusion or scheduling problems Further

examples are abstractions of systems where the processes of the original systems actually depend on the parameter. The literature in this area has studied a wealth of computational models based on a variety of synchronization and communication primitives including token passing broadcast and guarded transitions. Often different terminology is used in the literature and results are based on implicit assumptions. In this book we introduce a computational model that unites the central synchronization and communication primitives of many models and unveils hidden assumptions from the literature. We survey existing decidability and undecidability results and give a systematic view of the basic problems in this exciting research area.

Advances in Computing and Communications, Part II Ajith Abraham, Jaime Lloret Mauri, John Buford, Junichi Suzuki, Sabu M. Thampi, 2011-07-08. This volume is the second part of a four volume set CCIS 190 CCIS 191 CCIS 192 CCIS 193 which constitutes the refereed proceedings of the First International Conference on Computing and Communications ACC 2011 held in Kochi India in July 2011. The 72 revised full papers presented in this volume were carefully reviewed and selected from a large number of submissions. The papers are organized in topical sections on database and information systems distributed software development human computer interaction and interface ICT internet and Web computing mobile computing multi agent systems multimedia and video systems parallel and distributed algorithms security trust and privacy.

Autocar, 2005 Multicore Systems On-Chip: Practical Software/Hardware Design Abderazek Ben Abdallah, 2013-07-20. System on chips designs have evolved from fairly simple uniprocessor single memory designs to complex heterogeneous multicore SoC architectures consisting of a large number of IP blocks on the same silicon. To meet high computational demands posed by latest consumer electronic devices most current systems are based on such paradigm which represents a real revolution in many aspects in computing. The attraction of multicore processing for power reduction is compelling. By splitting a set of tasks among multiple processor cores the operating frequency necessary for each core can be reduced allowing to reduce the voltage on each core. Because dynamic power is proportional to the frequency and to the square of the voltage we get a big gain even though we may have more cores running. As more and more cores are integrated into these designs to share the ever increasing processing load the main challenges lie in efficient memory hierarchy scalable system interconnect new programming paradigms and efficient integration methodology for connecting such heterogeneous cores into a single system capable of leveraging their individual flexibility. Current design methods tend toward mixed HW SW co designs targeting multicore systems on chip for specific applications. To decide on the lowest cost mix of cores designers must iteratively map the device's functionality to a particular HW SW partition and target architectures. In addition to connect the heterogeneous cores the architecture requires high performance complex communication architectures and efficient communication protocols such as hierarchical bus point to point connection or Network on Chip. Software development also becomes far more complex due to the difficulties in breaking a single processing task into multiple parts that can be processed separately and then reassembled later. This reflects the fact that certain processor jobs cannot be easily parallelized to run concurrently.

on multiple processing cores and that load balancing between processing cores especially heterogeneous cores is very difficult *Electrical World* ,1922 **The Japan Daily Mail** ,1890 *Motor Industry Magazine* ,2007-02 **Cybernetics**
Yoshiyuki Sankai,Kenji Suzuki,Yasuhisa Hasegawa,2014-02-12 Cybernetics plays a significant role in coping with an aging society using state of the art technologies from engineering clinical medicine and humanities This new interdisciplinary field studies technologies that enhance strengthen and support physical and cognitive functions of human beings based on the fusion of human machine and information systems The design of a seamless interface for interaction between the interior and exterior of the human body is described in this book from diverse aspects such as the physical neurophysiological and cognitive levels It is the first book to cover the many aspects of cybernetics allowing readers to understand the life support robotics technology for the elderly including remote in home hospital institutional community medical welfare and vital sensing systems Serving as a valuable resource this volume will interest not only graduate students scientists and engineers but also newcomers to the field of cybernetics *Cycle World* ,1977 Government Reports Annual Index ,1980 Sections 1 2
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