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Computer Organization V. C. Hamacher, Zvonko G. Vranesic, Safwat G. Zaky, 1991 This third edition of the best selling text for computer organization courses takes a hardware oriented approach Not presuming knowledge of microelectronics the material is particularly suited to the undergraduate introductory course and for professional review **Embedded SoPC Design with Nios II Processor and VHDL Examples** Pong P. Chu, 2011-09-26 The book is divided into four major parts Part I covers HDL constructs and synthesis of basic digital circuits Part II provides an overview of embedded software development with the emphasis on low level I O access and drivers Part III demonstrates the design and development of hardware and software for several complex I O peripherals including PS2 keyboard and mouse a graphic video controller an audio codec and an SD secure digital card Part IV provides three case studies of the integration of hardware accelerators including a custom GCD greatest common divisor circuit a Mandelbrot set fractal circuit and an audio synthesizer based on DDFS direct digital frequency synthesis methodology The book utilizes FPGA devices Nios II soft core processor and development platform from Altera Co which is one of the two main FPGA manufactures Altera has a generous university program that provides free software and discounted prototyping boards for educational institutions details at www.altera.com/university The two main educational prototyping boards are known as DE1 99 and DE2 269 All experiments can be implemented and tested with these boards A board combined with this book becomes a turn key solution for the SoPC design

experiments and projects Most HDL and C codes in the book are device independent and can be adapted by other prototyping boards as long as a board has similar I O configuration

Embedded SoPC Design with Nios II Processor and Verilog Examples Pong P. Chu,2012-05-14 Explores the unique hardware programmability of FPGA based embedded systems using a learn by doing approach to introduce the concepts and techniques for embedded SoPC design with Verilog An SoPC system on a programmable chip integrates a processor memory modules I O peripherals and custom hardware accelerators into a single FPGA field programmable gate array device In addition to the customized software customized hardware can be developed and incorporated into the embedded system as well allowing us to configure the soft core processor create tailored I O interfaces and develop specialized hardware accelerators for computation intensive tasks Utilizing an Altera FPGA prototyping board and its Nios II soft core processor Embedded SoPC Design with Nios II Processor and Verilog Examples takes a learn by doing approach to illustrate the hardware and software design and development process by including realistic projects that can be implemented and tested on the board Emphasizing hardware design and integration throughout the book is divided into four major parts Part I covers HDL and synthesis of custom hardware Part II introduces the Nios II processor and provides an overview of embedded software development Part III demonstrates the design and development of hardware and software of several complex I O peripherals including a PS2 keyboard and mouse a graphic video controller an audio codec and an SD secure digital card Part IV provides several case studies of the integration of hardware accelerators including a custom GCD greatest common divisor circuit a Mandelbrot set fractal circuit and an audio synthesizer based on DDFS direct digital frequency synthesis methodology While designing and developing an embedded SoPC can be rewarding the learning can be a long and winding journey This book shows the trail ahead and guides readers through the initial steps to exploit the full potential of this emerging methodology

Computer Systems Ata Elahi,2022-03-16 This updated textbook covers digital design fundamentals of computer architecture and ARM assembly language The book starts by introducing computer abstraction basic number systems character coding basic knowledge in digital design and components of a computer The book goes on to discuss information representation in computing Boolean algebra and logic gates and sequential logic The book also presents introduction to computer architecture Cache mapping methods and virtual memory The author also covers ARM architecture ARM instructions ARM assembly language using Keil development tools and bitwise control structure using C and ARM assembly language The book includes a set of laboratory experiments related to digital design using Logisim software and ARM assembly language programming using Keil development tools In addition each chapter features objectives summaries key terms review questions and problems

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such as square root extraction and evaluation of exponential logarithmic and trigonometric functions The algorithms described are independent of the particular technology employed for their implementation

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