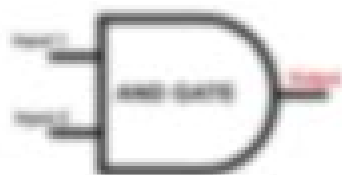


AND gate

❖ Symbol:



❖ Function:

It is a basic digital logic gate that implements logical conjunction – it behaves according to the truth table to the right. A HIGH output (1) results only if all the inputs to the AND gate are HIGH (1). If none or not all inputs to the AND gate are HIGH, a LOW output results. The function can be extended to any number of inputs.

❖ Truth Table:

2 input AND gate		
A	B	A.B
0	0	0
0	1	0
1	0	0
1	1	1

OR gate

❖ Symbol:



❖ Function:

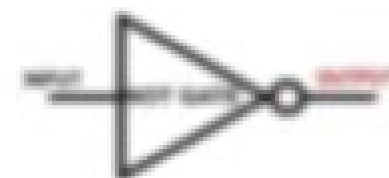
This is a digital logic gate that implements logical disjunction – it behaves according to the truth table to the right. A HIGH output (1) results if one or both the inputs to the gate are HIGH (1). If neither input is high, a LOW output (0) results.

❖ Truth Table:

2 input OR gate		
A	B	A+B
0	0	0
0	1	1
1	0	1
1	1	1

NOT gate

❖ Symbol:



❖ Function:

Often called an inverter, is a basic digital logic gate to start with because it has only a single input with simple behavior. A NOT gate performs logical negation on its input. In other words, if the input is true, then the output will be false. Similarly, a false input results in a true output.

❖ Truth Table:

NOT gate	
A	A'
0	1
1	0

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Rishabh Anand



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