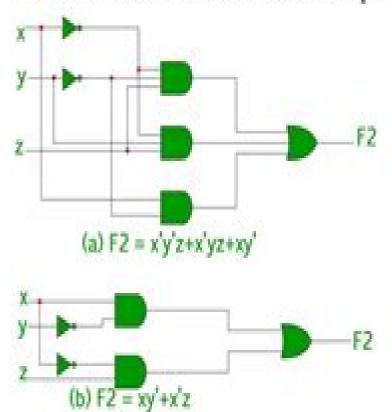


Need of Minimization



- For example, the function F= X.Y.Z+ X.Y.Z+X.Y can be minimized to X.Z+X.Y
- The circuits associated with above expressions is –



- ❖It is clear from the adjacent image that the minimized version of the expression takes a less number of logic gates and also reduces the complexity of the circuit substantially.
- Minimization is hence important to find the most economic equivalent representation of a Boolean function.

Solution Manual Switching Theory And Logic Gates

Donald J. Ecobichon, Eugene D Fabricius

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