

Chapter – 13

Intel 8086 Microprocessor architecture, features, and signals

Think & Answer type Questions:

1. How much memory in terms of bytes can be interfaced with 8086? Why?
Memory with a capacity of 1 MB can be interfaced with 8086 as it has 20 address lines (since $2^{20} = 1 \text{ MB}$).
2. What is the minimum and maximum size of a segment in terms of bytes? Why?
The minimum size of a segment can be one byte. The maximum size of a segment is 64 KB as the offset address size is 16 bits (since $2^{16} = 64\text{KB}$).
3. Why memory is divided into segments in 8086? What are its advantages?
The 8086 accesses each memory location using the content of a segment register which indicates the base address of a memory segment and an offset register which indicates the offset within the memory segment from where the data or instruction is accessed. Hence the memory is divided into segments.

The memory segmentation allows us to write relocatable program and data, by which the program and data to be executed by the 8086, can be placed anywhere in the memory map of 8086 (i.e. from the address 00000H to FFFFFH), and can be executed without any modification in the program and data. It also leads to good data structure since the program, data and stack are kept in separate segments.

4. How many 8K x 8 memory chips are required to construct 1 MB memory?
1 MB = 2^{20} bytes = $2^{20} \times 8$ bits
Each 8K x 8 memory chip can store $2^{13} \times 8$ bits.
Therefore, Number of 8K x 8 memory chips needed = $(2^{20} \times 8) / (2^{13} \times 8) = 2^7 = 128$
5. Which pin of 8086 determines mode of operation? How?
The $\overline{\text{MN}}/\overline{\text{MX}}$ pin of 8086 determines the mode of operation of 8086. If it is connected to +5V, 8086 operates in minimum mode and if it is connected to 0V (i.e. Ground), 8086 operates in maximum mode.
6. What are the differences between NMI and INTR interrupts in 8086?
The NMI interrupt is a non-maskable interrupt and it can not be disabled by software. The NMI interrupt has higher priority over INTR interrupt. The NMI interrupt is a vectored interrupt as it has a specific interrupt type number allotted to it, which is type 02H.

The INTR interrupt is a maskable interrupt and it can be enabled or disabled by setting or clearing the 'I' flag in the flag register respectively. The INTR interrupt has lower priority over NMI interrupt. The INTR interrupt is a non-vectored interrupt as it has no specific interrupt type number allotted to it by default.

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Solutions Manual to Microprocessor System Engineering Eccles,1985-01 **Instructor's Solutions Manual for Computer Architecture from Microprocessors to Supercomputers** Behrooz Parhami,2005 *Instructor's Manual for Microprocessors and Microcomputer-based System Design* Mohamed Rafiquzzaman,1995-08 **Microprogramming and Computer Architecture** Bruce Segee,John Field,1991 Presents the fundamentals a state machine with an ALU a simple CPU with an 8 bit data bus a bit slice based CPU that allows interrupts bus sharing asynchronous data transfers All circuits are built using real devices with reference made to real data manuals giving the text a more practical slant The Publishers' Trade List Annual ,1991 **Computer Architecture** William Gilreath,2023-01-08 Computer Architecture A Minimalist Perspective Exercise Solutions Manual provides answers and solutions to the seventy exercise problem questions in the original text The book includes an index for the diagrams equations examples and tables used in the solutions to the exercise problems Over four hundred references are available for the exercise solutions The book website <https://www.caamp.info> provides further information about the original text that the exercise solutions manual provides solutions , **The 8088 Microprocessor** Avtar Singh,Walter A. Triebel,1989 **Transactions on High-Performance Embedded Architectures and Compilers I** Mike O'Boyle,Francois Bodin,Marcelo Cintra,Sally A. McKee,2007-07-21 Transactions on HiPEAC is a new journal which aims at the timely dissemination of research contributions in computer architecture and compilation methods for high performance embedded computer systems It publishes original research on systems targeted at specific computing tasks as well as systems with broad application bases Its scope covers all aspects of computer architecture code generation and compiler optimization methods *The 8088 and 8086 Microprocessors* Walter A. Triebel,Avtar Singh,2000 For one semester courses in Microprocessors This text provides a systems level understanding of the 80X86 microprocessor and its hardware and software Equal emphasis is given to both assembly language software and microcomputer circuit design **Solutions Manual to Accompany Microprogramming and Computer Architecture** Field,1991-01-09 **Bowker's Complete Sourcebook of Personal Computing, 1985** R.R. Bowker Company,1984 Provides Listings of Hardware Software Peripherals Currently Available as Well as Books Magazines Clubs User Groups Virtually All Other Microcomputer related Services Includes Background Information Glossary *Computer Books and Serials in Print* ,1985 **Microprocessor Programming, Troubleshooting, and Interfacing the Z80, 8080, and 8085** James Coffron,1988 *Solution of Partial Differential Equations on Vector and Parallel Computers* James M. Ortega,Robert G. Voigt,1985-01-01 This volume reviews in the context of partial differential equations algorithm development that has been specifically aimed at computers that exhibit some form of parallelism Emphasis is on the solution of PDEs because these are typically the problems that generate high computational demands The authors discuss architectural features of these computers insomuch as they influence algorithm performance and provide insight into algorithm characteristics that allow

effective use of hardware Books in Print Supplement ,2002 **A Practitioner's Guide to RISC Microprocessor Architecture** Patrick H. Stakem,1996-04-25 Reduced Instruction Set Computers RISC reduce the number of instructions performed by the microprocessor This volume provides an overview of RISC as both a design philosophy and a marketing and technical force It introduces the fundamentals of RISC mic Readings in Computer Architecture Mark D. Hill,Gurindar S. Sohi,2000 Offering a carefully reviewed selection of over 50 papers illustrating the breadth and depth of computer architecture this text includes insightful introductions to guide readers through the primary sources **The VLSI Handbook** Wai-Kai Chen,2018-10-03 For the new millenium Wai Kai Chen introduced a monumental reference for the design analysis and prediction of VLSI circuits The VLSI Handbook Still a valuable tool for dealing with the most dynamic field in engineering this second edition includes 13 sections comprising nearly 100 chapters focused on the key concepts models and equations Written by a stellar international panel of expert contributors this handbook is a reliable comprehensive resource for real answers to practical problems It emphasizes fundamental theory underlying professional applications and also reflects key areas of industrial and research focus WHAT S IN THE SECOND EDITION Sections on Low power electronics and design VLSI signal processing Chapters on CMOS fabrication Content addressable memory Compound semiconductor RF circuits High speed circuit design principles SiGe HBT technology Bipolar junction transistor amplifiers Performance modeling and analysis using SystemC Design languages expanded from two chapters to twelve Testing of digital systems Structured for convenient navigation and loaded with practical solutions The VLSI Handbook Second Edition remains the first choice for answers to the problems and challenges faced daily in engineering practice **The 80386, 80486, and Pentium Processors** Walter A. Triebel,1998 This book is the first to concentrate on all 32 bit microprocessors and the pentium This comprehensive exploration of microprocessor technology introduces core concepts techniques and applications using the 80386 80486 and Pentium processors putting equal emphasis on assembly language software programming and microcomputer hardware interfacing The second part of this book presents software memory circuits I 0 and peripherals The third part consists of PC AT business interfacing testing troubleshooting and the pentium For anyone interested in Microprocessor Technology

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