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automates the entire digital design flow from Register Transfer Level RTL to Graphic Data System II GDSII making it an ideal tool for teaching and learning the physical design process Caravel on the other hand serves as an open source System on a Chip SoC platform allowing designers to integrate and test their designs in a versatile real world environment It complements OpenLane by enabling users to package and validate their designs bridging the gap between theoretical knowledge and practical implementation Together these tools provide a way to understand the full tape out process in a way that is accessible to students researchers and professionals alike <u>Design Automation</u> Michael Bushnell, 2012-12-02 Design Automation Automated Full Custom VLSI Layout Using the ULYSSES Design Environment deals with the use of the Ulysses design environment for an automated full custom VLSI layout Topics covered include VLSI chip design and design process control mechanisms in Ulysses and the use of artificial intelligence AI in design environments An example design task is also presented This book is comprised of 10 chapters and begins with an overview of VLSI computer aided design CAD focusing on an expert system based design environment aimed at solving the CAD tool integration problem An example CAD tool suite for such an environment is presented The next chapter describes prior attempts at developing an integrated design environment followed by a discussion on the computer aided VLSI design process that motivated the development of the Ulysses design environment The following chapters explore the use of AI techniques within Ulysses the fundamental architecture of Ulysses and the control mechanisms that govern the decision to execute various CAD tools on particular files within Ulysses The implementation of Ulysses is also discussed The final chapter demonstrates the feasibility of a knowledge based design environment for VLSI chip design applications the success of Ulysses at further automating the VLSI design process and the usability of Ulysses as a VLSI design environment This monograph will be a valuable resource for systems designers and other practitioners in computer science and computer engineering
Computer-Aided Design of Analog Integrated Circuits and Systems Rob A. Rutenbar, Georges G. E. Gielen, 2002-05-06 The tools and techniques you need to break the analog design bottleneck Ten years ago analog seemed to be a dead end technology Today System on Chip SoC designs are increasingly mixed signal designs With the advent of application specific integrated circuits ASIC technologies that can integrate both analog and digital functions on a single chip analog has become more crucial than ever to the design process Today designers are moving beyond hand crafted one transistor at a time methods They are using new circuit and physical synthesis tools to design practical analog circuits new modeling and analysis tools to allow rapid exploration of system level alternatives and new simulation tools to provide accurate answers for analog circuit behaviors and interactions that were considered impossible to handle only a few years ago To give circuit designers and CAD professionals a better understanding of the history and the current state of the art in the field this volume collects in one place the essential set of analog CAD papers that form the foundation of today s new analog design automation tools Areas covered are Analog synthesis Symbolic analysis Analog layout Analog modeling and analysis Specialized analog simulation Circuit centering and

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