

VLSI Lab viva question with answers

27. Design a FSM which detects the sequence 10101 from a serial line without overlapping.

[Answer](#)

28. Design a FSM which detects the sequence 10101 from a serial line with overlapping.

[Answer](#)

29. Give the design of 8x1 multiplexer using 2x1 multiplexers.

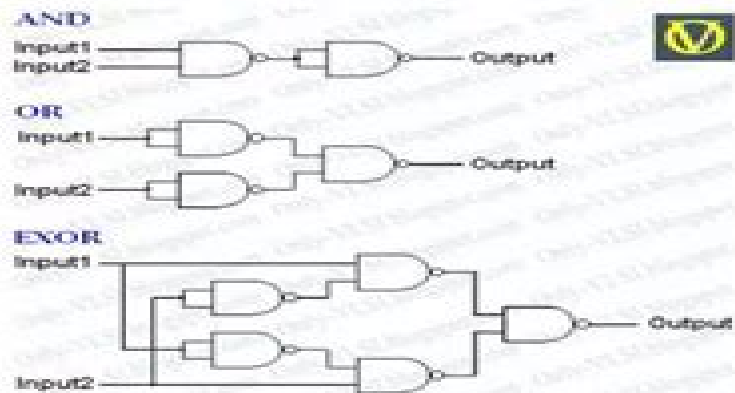
[Answer](#)

30. Design a counter which counts from 1 to 10 (Resets to 1, after 10).

[Answer](#)

31. Design 2 input AND, OR, and EXOR gates using 2 input NAND gate.

[Answer](#)



32. Design a circuit which doubles the frequency of a given input clock signal.

[Answer](#)

Vlsi Design Viva Questions With Answers

Arthur James Wells



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academicians in VLSI design electronics electrical engineering and materials science The basics and applications of VLSI design from STA PDA and VLSI Testing along with FPGA based Prototyping are covered in a comprehensive manner The latest technology used in VLSI design is discussed along with the available tools for FPGA prototyping as well as ASIC design Each unit contains technical questions with solutions at the end Technical topics discussed in the book include Static Timing Analysis CMOS Layout and Design rules Physical Design Automation Testing of VLSI Circuits Software tools for Frontend and Backend design

Study of Engineering and Career J Vinay Kumar, 2018-04-20 There are many ways to apply knowledge to achieve a successful career Different people have used different ideologies get to the top What are the characteristics that will help you achieve success This book caters not only to students stepping into the engineering fields or the corporate world for the first time but also to those who are stuck in the wrong profession The book highlights the importance of knowing your field of education the importance of personality finding the right opportunity in different fields of work choosing the right first employer and other important decisions related to your career This book is an essential read for anyone who wants to enter the field of engineering The volume includes a good number of illustrations with detailed notes

Gateway to VLSI Bharat Agarwal, Kshitij Goel, 2019-10-04 If you can spare half an hour then we can guarantee success at your next VLSI Very Large Scale Integration FPGA Field Programmable Gate Array STA Static Timing analysis interview Do you want to secure at least 3 to 4 job offers by succeeding at all the phone and on site job interviews for the FPGA DESIGN ENGINEER position Or do you simply want answers for the most frequently asked interview questions in VLSI FPGA digital circuit design Did you know that people who target question answer type preparation for a job interview are 3 4 times more likely to get a job offer than those who don't Did you also know that there is a set of questions that is likely to be repeatedly asked by interviewers across the industry no matter who you talk with in the VLSI FPGA digital design After a total of 17 unsuccessful interviews we thought of writing a book to help upcoming undergrads and experience professionals to get selected in such interviews The book covers every dimension related to FPGA Verilog STA and Protocols In simple words don't search anything on the internet this book is the Google of FPGA and Verilog *VLSI CAD Tools and Applications* Wolfgang Fichtner, Martin Morf, 2012-12-06 The summer school on VLSI CAD Tools and Applications was held from July 21 through August 1 1986 at Beatenberg in the beautiful Bernese Oberland in Switzerland The meeting was given under the auspices of IFIP WG 10.6 VLSI and it was sponsored by the Swiss Federal Institute of Technology Zurich Switzerland Eighty one professionals were invited to participate in the summer school including 18 lecturers The 81 participants came from the following countries Australia 1 Denmark 1 Federal Republic of Germany 12 France 3 Italy 4 Norway 1 South Korea 1 Sweden 5 United Kingdom 1 United States of America 13 and Switzerland 39 Our goal in the planning for the summer school was to introduce the audience into the realities of CAD tools and their applications to VLSI design This book contains articles by all 18 invited speakers that lectured at the summer school The reader should realize

that it was not intended to publish a textbook However the chapters in this book are more or less self contained treatments of the particular subjects Chapters 1 and 2 give a broad introduction to VLSI Design Simulation tools and their algorithmic foundations are treated in Chapters 3 to 5 and 17 Chapters 6 to 9 provide an excellent treatment of modern layout tools The use of CAD tools and trends in the design of 32 bit microprocessors are the topics of Chapters 10 through 16 Important aspects in VLSI testing and testing strategies are given in Chapters 18 and 19

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Physical Design Automation of VLSI Systems Bryan T. Preas, Michael J. Lorenzetti, Bryan D. Ackland, 1988

Examination Decrees and Regulations University of Oxford, 1990

Computerworld, 1983-05 For more than 40 years Computerworld has been the leading source of technology news and information for IT influencers worldwide Computerworld's award winning Web site Computerworld.com twice monthly publication focused conference series and custom research form the hub of the world's largest global IT media network

Designing Indoor Solar Products Julian Randall, 2006-02-03 Photovoltaic technology or the direct conversion of light into electricity is the fastest growing means of electricity generation today however it is generally used outdoors Relatively little attention has been focused on the many obstacles to overcome when designing efficient indoor products As a result indoor products are more often than not limited to low power Designing Indoor Solar Products bridges this gap by showing where AES Ambient Energy Systems based on photovoltaic cells may be used for higher power devices Motivated by both financial and ecological arguments this book Coordinates a wide reaching range of scientific information regarding photovoltaic technologies and their application to indoor spaces Analyses power management power availability technological selection and design methodologies Uses real life examples and case studies to demonstrate the arguments made Presents information in such a way as to make it accessible even to engineers with basic electrical knowledge Designing Indoor Solar Products pulls together a wealth of information on photovoltaic technologies and their applications It will be of practical interest to engineers and designers of sensor systems planning on using photovoltaic technology for power whilst the theoretical approach will appeal to those in academia in the related areas of environmental engineering sustainable development as well as building and product design

Digital VLSI Books Hilton Pickard, 2021-03-26 The book helps you to prepare digital VLSI interview questions It includes topics and concepts that the interviewer will ask Topics covered in this book 1 Digital Logic Design Number Systems Gates Combinational Sequential Circuits State Machines and

other Design problems 2 Computer Architecture Processor Architecture Caches Memory Systems 3 Programming Basics OOP UNIX Linux C C Perl 4 Hardware Description Languages Verilog SystemVerilog 5 Fundamentals of Verification Verification Basics Strategies and Thinking problems 6 Verification Methodologies UVM Formal Power Clocking Coverage Assertions 7 Version Control Systems CVS GIT SVN 8 Logical Reasoning Puzzles Related to Digital Logic General Reasoning Lateral Thinking 9 Non Technical and Behavioral Questions Most commonly asked

The Development of an Integrated Planning and Decision Support System (IPDSS) for Land Consolidation Demetris Demetriou, 2013-11-01 This book provides an extensive review of three interrelated issues land fragmentation land consolidation and land reallocation and it presents in detail the theoretical background design development and application of a prototype integrated planning and decision support system for land consolidation The system integrates geographic information systems GIS and artificial intelligence techniques including expert systems ES and genetic algorithms GAs with multi criteria decision methods MCDM both multi attribute MADM and multi objective MODM The system is based on four modules for measuring land fragmentation automatically generating alternative land redistribution plans evaluating those plans and automatically designing the land partitioning plan The presented research provides a new scientific framework for land consolidation planning both in terms of theory and practice by presenting new findings and by developing better tools and methods embedded in an integrated GIS environment It also makes a valuable contribution to the fields of GIS and spatial planning as it provides new methods and ideas that could be applied to improve the former for the benefit of the latter in the context of planning support systems From the 1960s ambitious research activities set out to observe regarding IT support of the complex and time consuming redistribution processes within land consolidation without any practically relevant results until now This scientific work is likely to close that gap This distinguished publication is highly recommended to land consolidation planning experts researchers and academics alike Prof Dr Ing Joachim Thomas Münster Germany Planning support systems take new scientific tools based on GIS optimisation and simulation and use these to inform the process of plan making and policy This book is one of the first to show how this can be consistently done and it is a triumph of demonstrating how such systems can be made operational Essential reading for planners analysts and GI scientists Prof Michael Batty University College London

INFORMS Annual Meeting Institute for Operations Research and the Management Sciences. National Meeting, 2004

Cracking Digital VLSI Verification Interview Robin Garg, Ramdas Mozhikunnath, 2016-03-13 How should I prepare for a Digital VLSI Verification Interview What all topics do I need to know before I turn up for an interview What all concepts do I need to brush up What all resources do I have at my disposal for preparation What does an Interviewer expect in an Interview These are few questions almost all individuals ponder upon before an interview If you have these questions in your mind your search ends here as keeping these questions in their minds authors have written this book that will act as a golden reference for candidates preparing for Digital VLSI Verification Interviews Aim of this book is to enable the readers

practice and grasp important concepts that are applicable to Digital VLSI Verification domain and Interviews through Question and Answer approach To achieve this aim authors have not restricted themselves just to the answer While answering the questions in this book authors have taken utmost care to explain underlying fundamentals and concepts This book consists of 500 questions covering wide range of topics that test fundamental concepts through problem statements a common interview practice which the authors have seen over last several years These questions and problem statements are spread across nine chapters and each chapter consists of questions to help readers brush up test and hone fundamental concepts that form basis of Digital VLSI Verification The scope of this book however goes beyond technical concepts Behavioral skills also form a critical part of working culture of any company Hence this book consists of a section that lists down behavioral interview questions as well Topics covered in this book 1 Digital Logic Design Number Systems Gates Combinational Sequential Circuits State Machines and other Design problems 2 Computer Architecture Processor Architecture Caches Memory Systems 3 Programming Basics OOP UNIX Linux C C Perl 4 Hardware Description Languages Verilog SystemVerilog 5 Fundamentals of Verification Verification Basics Strategies and Thinking problems 6 Verification Methodologies UVM Formal Power Clocking Coverage Assertions 7 Version Control Systems CVS GIT SVN 8 Logical Reasoning Puzzles Related to Digital Logic General Reasoning Lateral Thinking 9 Non Technical and Behavioral Questions Most commonly asked In addition to technical and behavioral part this book touches upon a typical interview process and gives a glimpse of latest interview trends It also lists some general tips and Best Known Methods to enable the readers follow correct preparation approach from day 1 of their preparations Knowing what an Interviewer looks for in an interviewee is always an icing on the cake as it helps a person prepare accordingly Hence authors of this book spoke to few leaders in the semiconductor industry and asked their personal views on What do they look for while Interviewing candidates and how do they usually arrive at a decision if a candidate should be hired These leaders have been working in the industry from many many years now and they have interviewed lots of candidates over past several years Hear directly from these leaders as to what they look for in candidates before hiring them Enjoy reading this book Authors are open to your feedback Please do provide your valuable comments ratings and reviews **Software Tools '89**, 1989 *Make It New* Barry M.

Katz, 2015-09-04 The role of design in the formation of the Silicon Valley ecosystem of innovation California's Silicon Valley is home to the greatest concentration of designers in the world corporate design offices at flagship technology companies and volunteers at nonprofit NGOs global design consultancies and boutique studios research laboratories and academic design programs Together they form the interconnected network that is Silicon Valley Apple products are famously Designed in California but as Barry Katz shows in this first ever extensively illustrated history the role of design in Silicon Valley began decades before Steve Jobs and Steve Wozniak dreamed up Apple in a garage Offering a thoroughly original view of the subject Katz tells how design helped transform Silicon Valley into the most powerful engine of innovation in the world From

Hewlett Packard and Ampex in the 1950s to Google and Facebook today design has provided the bridge between research and development art and engineering technical performance and human behavior Katz traces the origins of all of the leading consultancies including IDEO frog and Lunar and shows the process by which some of the world s most influential companies came to place design at the center of their business strategies At the same time universities foundations and even governments have learned to apply design thinking to their missions Drawing on unprecedented access to a vast array of primary sources and interviews with nearly every influential design leader including Douglas Engelbart Steve Jobs and Don Norman Katz reveals design to be the missing link in Silicon Valley s ecosystem of innovation Electronic Engineering
,1986 **International Books in Print 1990** Barbara Hopkinson,1990

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