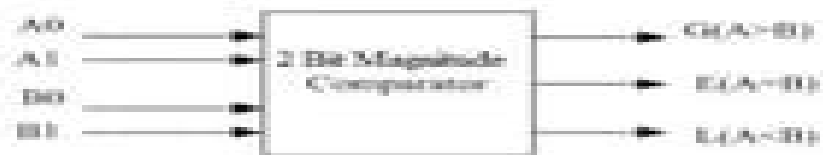


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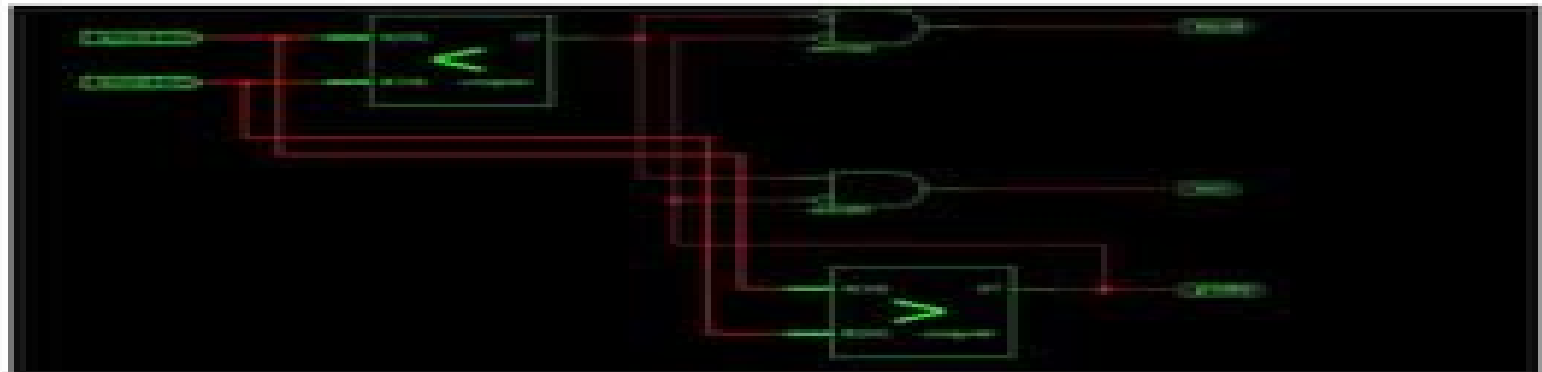
Block Diagram :



Truth Table :

Inputs	Inputs	Inputs	Inputs	Outputs	Outputs	Outputs
A ₁	A ₀	B ₁	B ₀	A < B	A = B	A > B
0	0	0	0	0	1	0
0	0	0	1	1	0	0
0	0	1	0	1	0	0
0	0	1	1	1	0	0
0	1	0	0	0	0	1
0	1	0	1	0	1	0
0	1	1	0	1	0	0
0	1	1	1	1	0	0
1	0	0	0	0	0	1
1	0	0	1	0	0	1
1	0	1	0	0	1	0
1	0	1	1	1	0	0
1	1	0	0	0	0	1
1	1	0	1	0	1	0
1	1	1	0	0	0	1
1	1	1	1	0	1	0

Logic Circuit Diagram :



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Cherry Bhargava



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Journal of VLSI Signal Processing Systems for Signal, Image, and Video Technology, 1997 Proceedings of Frontiers in Education 1996 IEEE, Education Society Staff, Ieee Education Society, 1996-11 **High-Performance VLSI Signal Processing Innovative Architectures and Algorithms, Algorithms and Architectures** K. J. Ray Liu, Kung Yao, 1998 Electrical Engineering Signal Processing High Performance VLSI Signal Processing Innovative Architectures and Algorithms Volume 1 Algorithms and Architectures The first volume in a two volume set High Performance VLSI Signal Processing Innovative Architectures and Algorithms brings together the most innovative papers in the field focused introductory material and extensive references The editors present timely coverage of algorithm and design methodologies with an emphasis on today's rapidly evolving high speed architectures for VLSI implementations These volumes will serve as vital resources for engineers who want a comprehensive knowledge of the extremely interdisciplinary field of high performance VLSI processing The editors provide a practical understanding of the merits of total system design through an insightful synergistic presentation of methodology architecture and infrastructure Each volume features Major papers that span the wide range of research areas in the field Chapter introductions including historical perspectives Numerous applications oriented design examples Coverage of current and future technological trends Thorough treatment of high speed architectures **Architectural Macro-modeling of Processor Memory Components** Ghazanfar Ali Khan, 1995
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