# Weslandia

### by PAUL FLEISCHMAN illustrated by KEVIN HAWKES

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### Study Guide Questions

Read each question below. Circle the letter that matches your answer choice

#### Qt. What two foods did Wesley disblue?

- at Prime and gods.
- by Bungers and free
- cl Hotdogs and soda
- d) Pirma and fries

#### Q2. What did he think about professional flotball?

- a) That he wanted to try it out
- to That it was cool
- of That it was stupid
- d) That it was fun

#### QB. He'd refused to share his head, though his father bribed him

- at \$1,000
- 100 \$30 00
- c) \$5.00
- d0 97 00

## **Weslandia Study Guide Answers**

J Elliott

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the device master record procedure prescribes the responsibilities for preparing device master records dmr and to ensure

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jun 29 2021 on january 31 1961 nasa launched ham the chimp into suborbital flight in an mr 2 rocket making him the first chimpanzee in space while the soviets had sent dogs into space before those missions merely tried to see if they could survive the trip

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story september 20 2023 on may 5 1961 alan shepard became the first american in space however three months earlier nasa had launched number 65 on a mission that helped pave the way for shephard s momentous flight number 65 was a male chimpanzee born in 1957 in the french cameroons in west africa

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sep 26 2019 ham the first astrochimp the skeleton of chimpanzee ham is part of the collections at the national museum of health and medicine ham completed a suborbital spaceflight in a mercury capsule on jan 31 1961 he died at 26 years old and his skeleton was donated to nmhm by the united states air force and the smithsonian national zoo

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#### chimpanzee ham with trainers nasa - Mar 15 2023

a three year old chimpanzee named ham in the biopack couch for the mr 2 suborbital test flight hams training took place in part at cape canaveral s hangar s on jan 31 1961 a mercury redstone launched from cape canaveral carrying the ham over

400 miles down range in an arching trajectory that reached a peak of 158 miles above the

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dec~16~2013 name ham the chimp species pan troglodytes dates 1957 1983 claim to fame first primate in space go visit national museum of health and medicine washington d c

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the success of ham s flight helped ratchet up even further the already frantic contest for scientific and space supremacy between the u s and the soviet union and briefly made ham something of a star here life com commemorates ham s 16 minute suborbital mission with photos taken before during and after his wild ride

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web the i 2c module contains an independent i 2c master logic and a i 2c slave logic which generates interrupts based on their events in the multi master systems the user software is simply partitioned into the master controller and the slave controller when the i 2c master logic is active the slave logic also remains active detecting the <a href="https://dsci2c.library.org/dsci2c.library.o

web i2c master h this header contains i o definitions selection and timing clock speed settings used for the master i2c interface this file also includes prototypes of master i2c functions i2c master c this source file contains master i2c function implementations file

i<sup>2</sup>c slave mode microchip technology - Apr 19 2022

web i2c module modes and features the i2c module provides the following operational modes and features master mode slave mode with byte nacking multi master mode dedicated receive and transmit buffers up to four dedicated slave address registers 1

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web the i 2 c master is byte oriented and interrupt based the number of interrupts generated is kept at a minimum by automatic handling of most incidents the software driver complexity and code size are reduced by auto triggering of operations and a special smart mode which can be enabled by the smart mode enable bit in the control b register

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web this example shows how the microcontroller configured in i 2 c host mode writes to and reads data from an mcp23008 8 bit i 2 c i o expander client device addressed in 7 bit mode using interrupts

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web 1 i2c specification 2 i2c module overview 3 interrupts for address match transmit buffer empty receive buffer full bus time out data byte count acknowledge and not acknowledge 4 i2c master mode operation 5 bus free time 6 master mode configuration and operation 7 master mode transmission 8 master mode reception 9

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web control of the i 2 c bus may be taken when the p bit is set or the bus is idle in firmware controlled master mode user code conducts all i 2 c bus operations based on start and stop bit condition detection start and stop condition detection is **i2c c master microchip technology** - Oct 06 2023

web lbidirectional i2c stands for inter integrated circuit communications i2c is implemented in the picmicro by a hardware module called the master synchronous serial port known as the mssp module

software implementation of i 2 c bus master microchip technology - Dec 28 2022

web c bus is a two wire serial bus with multiple possible masters and multiple possi ble slaves connected to each other through two wires the two wires consists of a clock line scl and a data line sda with both lines being bi directional click to browse repositories tb3281 microchip technology - Sep 24 2022

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web nov 7 2016 ds90003159b page 2 2017 microchip technology inc i2c protocol overview the i2c module follows the phillips i2c specification the module provides a bidirectional master slave syn chronous interface between the pic microcontroller and other i2c supported devices these devices are connected via a two wire serial bus using the mssp in i2c slave mode microchip technology - Oct 26 2022

web the master synchronous serial port mssp is an integrated serial communications module the mssp contains two sub modules spi serial peripheral interface i2c inter integrated circuit the inter integrated circuit commonly referred to as i2c is a synchronous two wire bidirectional serial communications bus

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web jun 23 2016 key features of the i2c module include the following independent master and slave logic multi master support which prevents message losses in arbitration detects 7 bit and 10 bit device addresses with configurable address masking in slave mode detects general call addresses as defined in the i2c protocol automatic sclx clock getting started with  $i^2c$  using mssp on pic18 microchip technology - Jun 02 2023

web the i2c bus is a multi master serial data communication bus microcontrollers communicate in a master slave environment where the master devices initiate the communication and the devices are selected through addressing i2c operates with one or more master devices and one or more slave devices

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