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## First/Second Semester B.E. Degree(CBCS)Examination

## Basic Electronics

Time: 3 hrs.

Max. Marks: 80

Note: Answer any FIVE full questions, choosing one full question from each module.

Module-1

- 1 a. Define following diode parameters: (06 Marks)  
 i) PIV ii) Knee voltage iii) Dynamic resistance.  
 b. With neat circuit diagram and waveforms, explain the working of a half wave rectifier. (06 Marks)  
 c. Draw common emitter circuit and sketch the input and output characteristics. Also explain operating regions by indicating them on characteristics curve. (04 Marks)

## OR

- 2 a. Explain zener diode voltage regulator circuit with no load and with load. (06 Marks)  
 b. The i/p to the Full wave rectifier is  $v(t) = 200 \sin 50t$ . If  $R_L$  is  $1k\Omega$  and forward resistance of diode is  $50\Omega$ , find: (06 Marks)  
 i) D.C current through the circuit  
 ii) The A.C (rms) value of current through the circuit  
 iii) The D.C output voltage  
 iv) The A.C power input  
 v) The D.C power output  
 vi) Rectifier efficiency.  
 c. Derive the relationship between  $\alpha$  and  $\beta$ . Calculate the value of  $I_c$ ,  $I_e$  for a transistor that has  $\alpha = 0.98$  and  $I_b = 100\mu A$ . (04 Marks)

Module-2

- 3 a. What is a Dc load line? Explain Base biased method with necessary equations. (05 Marks)  
 b. Explain the characteristics of an Ideal Op-amp. Mention two applications of Op-Amp. (06 Marks)  
 c. With a neat circuit diagram, explain the Voltage Divider Bias circuit using approximate analysis. (05 Marks)

## OR

- 4 a. Design the voltage divider bias circuit to operate from 12 V supply. The bias conditions are  $V_{CE}=3V$ ,  $V_E=5V$ , and  $I_c = 1mA$ . (05 Marks)  
 b. Derive the expression of 3 input summing amplifier. (05 Marks)  
 c. Explain voltage follower with neat circuit and necessary equations. (06 Marks)

Module-3

- 5 a. Convert  $(1101101)_2 = ( \quad )_{10}$  and  $(69)_{10} = ( \quad )_2$ . (04 Marks)  
 b. Convert  $(1010111011110101)_2 = ( \quad )_{10}$  and  $(FA876)_{16} = ( \quad )_2$ . (04 Marks)  
 c. State and prove Demorgan's Theorem. (08 Marks)

# Vtu Basic Electronics Question Papers

**Shasha Hu**



## **Vtu Basic Electronics Question Papers:**

**Basic Electronics - Second Edition** B Basavaraj, 2009-11-01 This is an established textbook on Basic Electronics for engineering students It has been revised according to the latest syllabus The second edition of the book includes illustrations and detailed explanations of fundamental concepts with examples The entire syllabus has been covered in 12 chapters

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Necessary Many Problems Objective Type And Review Questions Which Are Typically Set In Examinations Are Included In The Book At The End Of Each Chapter *BASIC ELECTRONICS*. VIJAY KUMAR REDDY H.S. ARAVIND,2015 *Basic Electronics - Second Edition* B Basavaraj,2009-11-01 This is an established textbook on Basic Electronics for engineering students It has been revised according to the latest syllabus The second edition of the book includes illustrations and detailed explanations of fundamental concepts with examples The entire syllabus has been covered in 12 chapters **Basic Electronics: For BPUT** De,2011 Basic Electronics For BPUT has been designed as a comprehensive textbook for first year students of Biju Patnaik University of Technology Orissa It lays a strong foundation in the important concepts of electronics by breaking down complex topics into simple and manageable units The circuit diagrams tables and solved examples used to illustrate theoretical concepts make this book an ideal self study guide for students This book is mapped to the syllabus prescribed by BPUT and the addition of three solved university question papers will benefit students greatly *Basic Electrical Engineering Made Easy* Sehgal And Bihari,2010-01-01 Made Easy Series is developed with an objective of meeting the requirement of books that cover syllabi of important core engineering subjects focussing completely on the manner in which concepts will be tested in examinations Books in this series are designed in a question and answer format to cater to undergraduate students of all major technological universities and to equip them with the desired knowledge in a simple yet comprehensive manner They explore all the important concepts of the syllabi with the help of solved questions and numerical problems of previous years question papers of these universities Apart from being extremely student friendly and lucid the books in this series are rich in pedagogical features such as brief point wise discussion of fundamental concepts theoretical questions with answers solved numerical problems and objective questions and exercises for further practice all taken from previous years question papers that aid students in preparing well for university examinations Because of the fiercely competitive nature of the current academic scenario and the large number of books available for each topic it is extremely difficult for students to spend too much time in an in depth study of each book especially during examinations when they are hard pressed for time Made Easy Series will empower students to prepare for university examinations in a systematic and thorough manner in a limited amount of time The syllabi of the following universities have been covered in the book UPTU Anna Univ JNTU VTU RTU RGTU WBUT BPUT PTU Pune Univ Mumbai Univ *BASIC ELECTRONICS* Dr K A Navas,T. A. Suhail,2018-08-01 This book is prepared as per the syllabus of Basic Electronics for first year B Tech Engineering course under Visvesvaraya Technological University Karnataka using the reference books given in the course syllabus Authors have tried to elucidate the topics such a way that even a mediocre student can assimilate them Many solved problems sample question papers and exercise given in every section will provide a thorough understanding of topics Question Bank on Electrical and Electronics Engineering with Question Papers from Various Competitive and Recruitment Examinations C. Harish Rai,2020-03-30 This book follows a logical concept building approach rather than only formula based as offered by

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Standard electronics questions and answers Stephen Michael Elonka, Julian Lawrence Bernstein, 1964 DE05  
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